

Debug and Analysis for Electromagnetic Interference of Information Technology Equipment

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ABSTRACT

While designing the high speed digital system, PCB Layout analysis in advance is very important, because design engineer experience is not enough and the trace routing mistake and so on. Those factors caused the electromagnetic compatibility problems of the designed sample. This thesis provides a fast and simple assistant analysis method for electromagnetic compatibility modification. The disturbing traces which cause the electromagnetic compatibility can be determined by finger-touch, near field probe, conductive probe, and component removing and so on. This procedure search for the culprit traces from wide rang to the small scope and focus on these to carry out the corresponding measures to eliminate these causes, until the system conforms to the laws and regulations requirement.

Keywords : electromagnetic compatibility ; finger-touch ; near field probe ; conductive probe ; component removing

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