

Impact Analysis of the Metal Enclosure on RF Amplifier Circuit

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ABSTRACT

The advents of 3C products not only bring people more convenient life but also introduce complex electromagnetic (EM) noises. Electronic circuits not only introduce radiated interferences but they are also disturbed by the electromagnetic environment. Since interference may render electronic products malfunctioning and electronic devices should be designed not only to survival in noisy environment but also minimized interfering with the nearby devices. Analog circuits such as analog amplifier, signal transducer, and compensation circuit, are most sensitive to EM interferences. In order to reduce the influence of radiation interference, we shield the electronic products in metal box. This study will design a low noise amplifier and a power amplifier which can operate at 1GHz and 2GHz. Active components such as Infineon (Infineon Technologies AG) and Philips Company Secretary production transistors (BFP420) ,(BFG452W) ,(BFG21W) and high-frequency circuit design software (Advanced Design System 2004A) are needed in the design of the circuits. Impedance matching is facilitated using surface-mount device (SMD) lump elements. All circuits are implemented on FR4 printed circuit boards. The fabricated circuits were put into metallic boxes, whose effects on the circuit performance were analyzed and studied.

Keywords : Shield ; Low Noise Amplifier ; Power Amplifier ; Surface Mounted Device

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REFERENCES

- [1] ADS 2004A user ' s guide.
- [2] David M. Pozar, " Microwave Engineering " , Second Edition, Chapter 2,Wiley, 1998.
- [3] Samuel Y. Liao, " Microwave Circuit Analysis and Amplifier Design " , Chapter 4, Chapter 7, Prentice Hall, 1987.
- [4] Guillermo Gonzalez, " Microwave Transistor Amplifiers Analysis and Design " , Second Edition, Chapter 3, Chapter 4, Prentice Hall,1997.
- [5] George D. Vendelin and Anthony M. Pavio and Ulrich L. Rohde, " Microwave Circuit Design Using Linear and Nonlinear Techniques " , Chapter 4, Wiley, 1990.
- [6] Terry Edwards, " Foundations for Microstrip Circuit Design " , Second Edition, Chapter 3, Chapter 4, Chapter 5, Wiley, 1991.
- [7] Behzad Razavi, " RF Microelectronics " Section 2.3, Prentice Hall, 1998.
- [8] H. Rothe and W. Dahlke, " Theory of Noisy Fourpoles " , Proceeding of the I.R.E., Vol. 44, June 1956, pp. 811-818.
- [9] Silvester, P., and Benedek, P., " Microstrip discontinuity capacitances for right angle bands, T-junction and crossings " , IEEE Trans., MTT-21, No. 5, May 1973, 341-346.
- [10] Bahl, I. J., and Garg, Ramesh, " Simple and accurate formulas for microstrip with finite strip thickness " , Proc. IEEE, 65, pp. 1611-1612, 1977.
- [11] M. L. Edwards and J. H. Sinksy, " A New Criteria for Linear 2-Port Stability Using a single Geometrically Derived Parameter " , IEEE Trans. Microwave Theory and Techniques, vol. MTT-40, pp. 2803-2811, December 1992.
- [12] V. Paidi et al., " Simulations of high linearity and high efficiency of class B power amplifiers in GaN HEMT technology, " in IEEE Lester Eastman Conference on High Performance Devices, Aug. 2002.
- [13] Wei GUO and Daquan HUANG, " The Noise and Linearity Optimization for A 1.9-GHz CMOS Low Noise Amplifier " , 0-7803-7363-4/02/IEEE.
- [14] Steve C. Cripps, " RF Power Amplifiers for Wireless Communications " , Chapter2, Artech House, 1999.
- [15] Marian Kazimierczuk, " Effects of the Collector Current Fall Time on the Class E Tuned Power Amplifier " , IEE J. Solid-State Circuits, vol.SC-18, pp. 181-193, Apr. 1983.
- [16] E. Sacchi, I. Bietti, F.Svelto and R. Castello, " A 2dB NF, fully differential, variable gain, 900MHz CMOS LNA, " 2000 Symposium on VLSI Circuits Digest of Technical Papers, pp.94-97, 2000.
- [17] X. Li, H. Kim, M. Ismail and H. Olsson, " A novel design approach for GHz CMOS low noise amplifiers, " 1999 IEEE Radio and Wireless Conference, pp.285-288, 1999.
- [18] Frederick H. Raab, " Class-F Power Amplifier with Maximally Flat Waveforms " , IEE Trans. On MTT, vol. 45, pp. 2007-2012, Nov. 1997.
- [19] Statz, H., et al., " GaAs FET Device and Circuits Simulation in SPICE, " IEEE Trans. Electron Dev., ED-34, Feb. 1987,pp.160-169.
- [20] 袁帝文/王岳華/謝孟翰/ 王弘毅編著, " 高頻通訊電路設計 ",高立圖書 [21] 許敬恭, " 數位廣播電波間隙補強器整合設計與研究 " ,2004,大葉大學 [22] 育英科技有限公司, " 射頻電路設計實習 " ,滄海書局,90年.
- [23] 王參農, " 衛星定位雙系統之高增益低雜訊放大器設計與分析 " ,2005,大葉大學 [24] 許文昭, " DAB單頻網路之雙頻段整合中繼放大器 " ,2006,大葉大學 [25] 莊豐躋, " 微型EMI探棒之寬頻放大器設計分析 " ,2007,大葉大學 [26] 張佑誠, " 可控制增益寬頻功率放大器之設計與製作 " ,2003,逢甲大學 [27] 陳麒安, " 5.3GHz射頻放大器模組之設計與製作 " ,2003,中央大學 [28] 張盛富/張嘉展編著, " 射頻晶片模組設計 " ,全華圖書 [29] 傅延宗, " Design and Implementation of 2.4GHz RF Power Amplifier " ,中華大學.