

SiGe/Si 異質結構光電特性的探索

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摘要

本實驗中將利用具高能隙、高阻值及高吸收係數的非晶矽半導體材料覆蓋在矽鋅上，並探討其光電特性，首先是利用非晶矽半導體在矽鋅表面的保護層作用，降低表面缺陷所製作而成的平面式金屬-半導體-金屬結構的光檢測器，藉由此設計來降低暗電流，提高其光暗電流比，並比較其有/無非晶矽氫保護層結構與電漿增強化學氣相沈積氧化層結構的光電特性。在此我們發現具有非晶矽保護層的矽鋅光檢測器暗電流為 1.27×10^{-8} A與不具非晶矽氫保護層的矽鋅光檢測器其暗電流為 2.14×10^{-3} A，而具氧化物保護層的暗電流為 2.5×10^{-3} A，其具非晶矽氫與不具非晶矽氫的暗電流相差五個數量級，由此可知非晶矽氫是一個很好的保護層，能藉由非晶矽氫覆蓋在矽鋅表面降低缺陷的特性應用在矽鋅光檢測器上達到有效降低暗電流的目的。在另一方面我們將沿用上述所發現的特性，製作成非晶矽氫/矽鋅/矽異質結構光電晶體。我們利用850nm波長的光源，改變其入射光功率，比較對於不同電極的光電特性，由實驗結果得知具銻錫氧化物電極的光增益較好。透過電晶體的作用，光電晶體可以有較高的電流增益，因此採用異質結構來製作光電晶體，異質結構光電晶體的能帶間隙差異會使得注入效率增大，提高電流增益。

關鍵詞：金屬-半導體-金屬；光檢測器；非晶矽氫；矽鋅；暗電流；光電晶體

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參考文獻

- [1] J. C. Campbell, C. A. Burrus, A. G. Dentai, K. Ogawa, "Small-area high-speed InP/InGaAs phototransistor," *Appl. Phys. Lett.*, Vol. 39, pp.820-821 (1981) [2] P. D. Wright, R. J. Nelson, T. Cellia, "High-gain InGaAsP-InP heterojunction phototransistors," *Appl. Phys. Lett.*, Vol. 37, pp.192-194 (1980) [3] Y. Wang, E. S. Yang, W. I. Wang, "High gain and wide dynamic range punchthrough heterojunction phototransistors," *J. Appl. Phys.*, Vol. 74, pp. 6978-6981 (1993) [4] Z. Huang, J. Oh, J. C. Campbell, "Back-side-illuminated high-speed Ge photodetector fabricated on Si substrate using thin SiGe buffer layers," *APPLIED PHYSICS LETTERS*, Vol. 85, pp. 2386-2388 (2004) [5] J. Oh, J. C. Campbell, S. G. Thomas, S. Bharatan, R. Thoma, C. Jasper, R. E. Jones, T. E. Zirkle, "Interdigitated Ge p-i-n Photodetectors Fabricated on a Si Substrate Using Graded SiGe Buffer Layers," *IEEE JOURNAL OF QUANTUM ELECTRONICS*, Vol. 38, pp. 1238-1241 (2002) [6] S. Y. Lo, Y. L. Wei, R. H. Yeh, J. W. Hong, "Suppressing dark-current in planar Si-based MSM photodetector with alternated i-a-Si:H/i-a-SiGe:H grade superlattice-like layers," *ELECTRONICS LETTERS*, Vol. 41 (2005) [7] C. S. Lin, L. P. Tu, R. H. Yeh, J. W. Hong, "High-Sensitivity Planar Si-Based MSM Photodetector With Very Thin Amorphous Silicon-Alloy Quantum-Well-Like Barrier Layers," *IEEE PHOTONICS TECHNOLOGY*

LETTERS, Vol. 15, pp.996-968 (2003) [8] D. Buca, S. Winnerl, S. Lenk, Ch. Buchala, D. X. Xu, "Fast time response from Si – SiGe undulating layer superlattices," APPLIED PHYSICS LETTERS, Vol.80, pp. 4172-4174 (2002) [9] F. G.. D. Corte, F. Pezzimenti, "Design Considerations for a-Si:H/SiGe/Si Heterojunction Bipolar Transistors," IEEE TRANSACTIONS ON ELECTRON DEVICES, Vol. 50, pp. 2180-2182 (2003) [10] N. Chand, "LWIP: a long-wavelength infra-red phototransistor," Electronics letters, Vol. 29, pp. 1800-1802 (1993) [11] Z. Pei, C. S. Liang, L. S. Lai, Y. T. Tseng, Y. M. Hsu, P. S. Chen, S. C. Lu, M. J. Tsai, C. W. Liu, "A High-Performance SiGe – Si Multiple-Quantum-Well Heterojunction Phototransistor," IEEE ELECTRON DEVICE LETTERS, Vol. 24, pp. 643-645 (2003) [12] Z. Pei, L. S. Lai, H. P. Hwang, Y. T. Tseng, C. S. Liang, M. J. Tsai, "Si_{1-x}Ge/Si multi-quantum well phototransistor for near-infrared operation," Physica E, Vol. 16, pp. 554-557 (2003) [13] Z. Pei, J. W. Shi, Y. M. Hsu, F. Yuan, C. S. Liang, S. C. Lu, W. Y. Hsieh, M. J. Tsai, C. W. Liu, "Bandwidth Enhancement in an Integratable SiGe Phototransistor by Removal of Excess Carriers," IEEE ELECTRON DEVICE LETTERS, Vol. 25, pp. 286-288 (2004) [14] B. S. Wu, C. Y. Chang, Y. Y. Fang, R. H. Lee, "Amorphous Silicon Phototransistor on a Glass Substrate," IEEE TRANSACTIONS ON ELECTRON DEVICES, Vol. ED-32, pp. 2192-2196 (1985) [15] C. Y. Chang, B. S. Wu, Y. K. Fang, R. H. Lee, "Optical and Electrical Current Gain in an Amorphous Silicon Bulk Barrier Phototransistor," IEEE ELECTRON DEVICE LETTERS, Vol. EDL-6, pp. 149-150 (1985) [16] C. Y. Chang, B. S. Wu, Y. K. Fang, R. H. Lee, "Amorphous silicon bulk barrier phototransistor with Schottky barrier emitter," Appl. phys. lett., Vol. 47, pp. 49-51 (1985) [17] S. B. Hwang, Y. K. Fang, K. H. Chen, C. R. Liu, J. D. Hwang, M. H. Chou, "An a-Si : H/a-Si_xGe : H Bulk Barrier Phototransistor with a-SiC : H Barrier Enhancement Layer for High-Gain IR Optical Detector," IEEE TRANSACTIONS ON ELECTRON DEVICES, Vol. 40, pp. 721-726 (1993) [18] T. Yin, A. M. Pappu, A. B. Apsel, "Low-Cost, High-Efficiency, and High-Speed SiGe Phototransistors in Commercial BiCMOS," IEEE PHOTONICS TECHNOLOGY LETTERS, Vol. 18, pp.55-57 (2006) [19] L. H. Laih, T. C. Chang, Y. A. Chen, W. C. Tsay, J. W. Hong, "Amorphous metal-semiconductor-metal photodetector (MSM-PD) with bottom ridged Cr electrodes," ELECTRONICS LETTERS, Vol. 35, pp.1022-1023 (1999) [20] L. H. Laih, W. C. Tsay, Y. A. Chen, T. S. Jen, R. H. Yuang, J. W. Hong, "High-performance metal-semiconductor-metal photodetector with a thin hydrogenated amorphous silicon layer on crystalline silicon," ELECTRONICS LETTERS, Vol. 31, pp. 2123-2124 (1995) [21] C. T. Lee, H. Y. Lee, "Metal-Semiconductor-Metal Photodetectors With InAlGaP Capping and Buffer Layers," IEEE ELECTRON DEVICE LETTERS, Vol. 24, pp. 532-534 (2003) [22] H. Y. Lee, C. T. Lee, "Metal-Semiconductor-Metal Photodetectors Using Widegap Semiconductor Capping Layer," IEEE, pp.15-18 (2003) [23] C. T. Lee, H. Y. Lee, "Surface Passivated Function of GaAs MSM-PDs Using Photoelectrochemical Oxidation Method," IEEE PHOTONICS TECHNOLOGY LETTERS, Vol. 17, pp. 462-464 (2005) [24] C. Y. Chang, "Photogeneration and Recombination in a Bulk Barrier Phototransistor," IEEE TRANSACTIONS ON ELECTRON DEVICES, Vol. ED-33, pp. 1829-1830 (1986) [25] S. J. Koester, B. U. Klepser, J. O. Chu, D. Kuchta, K. Ismail, "1.1GHz MSM Photodiodes on relaxed Si_{1-x}Ge grown by UHV-CVD," Device Research Conference, 56th (1998) [26] Y. Kimura, K. Nakagawa, M. Miyao, "The effect of surface segregation on the light-emission intensity of Si/SiGe/Si heterostructures," APPLIED PHYSICS LETTERS, Vol. 73, pp. 232-234 (1998) [27] J. D. Hwang, C. Y. Kung, Y. H. Chen, C. S. Wei, P. S. Chen, "Liquid phase deposition silicon dioxide for surface passivation in SiGe metal-semiconductor-metal photodetectors," Thin Solid Films, Vol. 515, pp. 4049-4052 (2007) [28] P. D. Wright, R. J. Nelson, T. Celli, "High-gain InGaAsP-InP heterojunction phototransistors," Appl. Phys. Lett., Vol. 37, pp. 192-194 (1980) [29] Y. Wang, E. S. Yang, W. I. Wang, "High gain and wide dynamic range punchthrough heterojunction phototransistors," J. Appl. Phys., Vol. 74, pp. 6978-6981 (1993) [30] H. Luo, Y. Chang, K. S. Wong, Y. Wang, "Ultrasensitive Si phototransistors with a punchthrough base," Appl. Phys. Lett., Vol. 79, pp.773-775 (2001) [31] R. PEOPLE, "Physics and Applications of Ge_xSi_{1-x}/Si Strained-Layer Heterostructures," IEEE JOURNAL OF QUANTUM ELECTRONICS, Vol. 22, pp.1696-1710 (1986) [32] H. Matsuura, T. Okuno, H. Okushi, K. Tanaka, "Electrical properties of n-amorphous/p-crystalline silicon heterojunctions," J. Appl. Phys., Vol. 55, pp. 1012-1019 (1984) [33] A. Nasceatti, D. Caputo, "Amorphous Silicon Phototransistor as Nonlinear Optical Device for High Dynamic Range Imagers," IEEE TRANSACTIONS ON ELECTRON DEVICES, Vol. 49, pp. 395-399 (2002) [34] S. M. SZE and KWOK K. NG, Physics of Semiconductor Devices, pp.667, pp.712 (2007)