

Analysis and design of the broadband amplifier for miniature EMI probe

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ABSTRACT

As electronic integrated circuits gradually become the trend, electromagnetic compatibility-related researches gradually focus on integrated circuit as well. However, for the measurement skills of integrated circuit sides, test probe of more miniature size must be developed to measure radiation levels accurately. In addition to the rapid attenuation of noise radiation field with distance, the chip-radiation is relatively weaker than general electronic circuits radiation field because of the small chip size, resulting in a difficulty in detecting level. To solve this problem, this study emphasizes on designing a broadband preamplifier circuit for miniature test probe, raising the measurement sensitivity, amplifying a weak signal and getting a more accurate measurement. This study will design a broadband low noise amplifier for related radiation testing standards bandwidth (30 MHz-1GHz). The active components is transistors (BFP420) from Infineon (Infineon Technologies AG) Company, along with high-frequency circuit design software (Advanced Design System 2004A) and RF circuits based on the principle of impedance matching. In this band, impedance matching using surface-mount device (SMD) lump elements, and the production of all circuits in FR4 printed circuit board, and finally the overall circuit using the network analyzer, spectrum analyzer, noise index analyzer, measurement of its overall circuit parameters, Finally 61967-3,61967-6 according to IEC standards such as IC EMC calibration procedures, be measured with the probe integration and use of the spectrum analyzer to compare test.

Keywords : IC EMC ; miniature probe ; Broadband and Low Noise Amplifier (LNA)

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REFERENCES

- [1] ADS 2004A user ' s guide.
- [2] David M. Pozar, " Microwave Engineering " , Second Edition, Chapter 2,Wiley, 1998.
- [3] Samuel Y. Liao, " Microwave Circuit Analysis and Amplifier Design " , Chapter 4, Chapter 7, Prentice Hall, 1987.
- [4] Guillermo Gonzalez, " Microwave Transistor Amplifiers Analysis and Design " , Second Edition, Chapter 3, Chapter 4, Prentice Hall,1997.
- [5] George D. Vendelin and Anthony M. Pavio and Ulrich L. Rohde, " Microwave Circuit Design Using Linear and Nonlinear Techniques " , Chapter 4, Wiley, 1990.
- [6] Terry Edwards, " Foundations for Microstrip Circuit Design " , Second Edition, Chapter 3, Chapter 4, Chapter 5, Wiley, 1991.
- [7] S. Silver, " Microwave Antenna Theory and Design " , pp. 389-395 , McGraw-Hill,1949.
- [8] H. Rothe and W. Dahlke, " Theory of Noisy Fourpoles " , Proceeding of the I.R.E., Vol. 44, June 1956, pp. 811-818.
- [9] Silvester, P., and Benedek, P., " Microstrip discontinuity capacitances for right angle bands, T-junction and crossings " , IEEE Trans., MTT-21, No. 5, May 1973, 341-346.
- [10] Bahl, I. J., and Garg, Ramesh, " Simple and accurate formulas for microstrip with finite strip thickness " , Proc. IEEE, 65, pp. 1611-1612, 1977.
- [11] M. L. Edwards and J. H. Sinksy, " A New Criteria for Linear 2-Port Stability Using a single Geometrically Derived Parameter " , IEEE Trans. Microwave Theory and Techniques, vol. MTT-40, pp. 2803-2811, December 1992.
- [12] Behzad Razavi, " RF Microelectronics " Section 2.3, Prentice Hall, 1998.
- [13] Wei GUO and Daquan HUANG, " The Noise and Linearity Optimization for A 1.9-GHz CMOS Low Noise Amplifier " , 0-7803-7363-4/02/IEEE.
- [14] Hafez Fouad, Khaled Sharaf, Essam El-Diwany, Hadia El-Hennawy, " AN RF CMOS CASCODE LNA WITH CURRENT REUSE AND INDUCTIVE SOURCE DEGENERATION " , 0-7803-7150-x /01/ IEEE.
- [15] Luca Daniel and Manolis Terrovitis " A Broadband Low-Noise Amplifier " , EECS217 " Microwave Circuit Design " .
- [16] K. L Walton and V. C. Sundberg, " Broadband Ridge Horn Design " , Microwave Journal Vol. 7, March 1964, pp. 96-101.
- [17] Seymour B. Cohn, " Properties of Ridge Wave Guide " , Proceeding of the I.R.E., August 1947, pp. 783-788.
- [18] IEC INTERNATIONAL STANDARD 61967-6,61967-3, First edition,2002-06.
- [19] Infineon Inc.NPN Silicon RF Transistor " BFP 420 " ,Data Sheet.
- [20] 袁帝文/王岳華/謝孟翰/ 王弘毅編著, " 高頻通訊電路設計 " ,高立圖書 [21] 許敬恭, " 數位廣播電波間隙補強器整合設計與研究 " ,2004,大葉大學 [22] 育英科技有限公司, " 射頻電路設計實習 " ,滄海書局,90年.
- [23] 王參農, " 衛星定位雙系統之高增益低雜訊放大器設計與分析 " ,2005,大葉大學 [23] 許文昭, " DAB單頻網路之雙頻段整合中繼放大器 " ,2006,大葉大學