

# VLSI Testing Technique

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## ABSTRACT

IC (Integrated Circuit) is the most basic component of electronics, which integrates the transistor, diode, resistor and capacitor into a chip and this chip is called Semiconductor. In recent years, semiconductor industry has been growing fast and the complexity of IC has been increasing, therefore chip testing has become more important in the process of semiconductor manufacture. What is the test engineering? The purpose of semiconductor testing is to verify the device function of design rule after chip fabricated, and to make it work normally even the chip is in worse conditions. The most important of test engineering concern is how to complete all test items with best shipping quality in low cost solution. There are some many kinds of semiconductor device. In roughly, it can separate into 4 kinds of device that include memory IC, logic IC, microprocessor and mixed signal IC. We will focus on memory and logic testing methodology in this paper. Also we will introduce the tester hardware structure and testing manufacture and then look into the testing theorem. Furthermore, we will use the true device for real example.

Keywords : Logic Testing ; Memory Testing ; Laser Repair ; Redundancy ; Test Pattern ; DC Testing ; AC Testing

## Table of Contents

封面內頁 簽名頁 授權書 . . . . .	iii	中文摘要 . . . . .	iii
. . . . . iv	英文摘要 . . . . .	v	誌謝 . . . . .
. . . . . vi	目錄 . . . . .	vii	圖目錄 . . . . .
. . . . . ix	表目錄 . . . . .	xi	第
第一章 緒論 1.1 研究背景與動機 . . . . .	1	1.2 論文架構 . . . . .	1
第二章 IC製造流程 2.1 生產流程 . . . . .	2	2.2 晶圓測試 . . . . .	3
2.3 成品測試 . . . . .	4	2.4 封裝 . . . . .	6
. . . . . 6	2.4.1封裝方法及目的 . . . . .	7	2.4.2封裝型態 . . . . .
. . . . . 11	2.4.3封裝製作流程 . . . . .	7	2.4.4封裝型態 . . . . .
第三章 測試系統架構分析 3.1邏輯測試系統 . . . . .	16	3.1.1測試系統基本架構 . . . . .	16
. . . . . 16	3.1.2測試系統硬體說明 . . . . .	18	3.2記憶體測試系統 . . . . .
. . . . . 23	3.2.1記憶體修補分析(MRA)單元 . . . . .	24	3.2.2記憶體修補輸出格式 . . . . .
第四章 產品測試原理探討 4.1邏輯產品測試方法 . . . . .	27	4.1.1直流特性測試 . . . . .	27
. . . . . 27	4.1.2交流特性測試 . . . . .	34	4.2記憶體產品測試方法 . . . . .
4.2.1記憶體IC基本構成 . . . . .	38	4.2.2記憶體IC測試形態 . . . . .	40
. . . . . 38	4.2.3記憶體備份電路修補方法 . . . . .	43	4.3.1備份電路(Redundancy)的意義 . . . . .
. . . . . 43	4.3.2雷射修補方法 . . . . .	44	4.3.3雷射修補方法 . . . . .
. . . . . 44	第五章 結果分析 5.1偵錯工具(Debug Tool) . . . . .	47	5.2異常分析 . . . . .
. . . . . 51	第六章 結論 . . . . .	55	參考文獻 . . . . .
. . . . . 56			

## REFERENCES

- [1]施敏, “半導體元件物理與製造技術”, 國立交通大學出版社, 2002.
- [2]張俊彥, “積體電路製程及設備技術手冊”, 經濟部技術處, 1997.
- [3]吳萬鈺, 陳竹一, “記憶體晶片測試工程簡介”, 電子月刊, 1996.
- [4]莊達人, “VLSI 製造技術”, 高立出版社, 2005.
- [5]福岡義孝, “電子構裝技術”, 普林斯頓國際, 2005.
- [6]梁明侃, “構裝可靠度之分析測試與不良分析”, 國立交通大學人才培訓中心, 2005.
- [7]陳霖富, “IC故障分析”, 自強基金會, 2005.
- [8]白中和, “半導體MOS記憶器及其使用技術”, 建興文化事業, 2001.
- [9]林大欽, “邏輯IC測試廠短期生產排程之探討”, 國立清華大學工業工程研究所論文, 1997
- [10]Credence Corporation, “SC Series Applications Training Workbook”, 1999.

- [11]Credence Corporation, “ Toolbox Digital Applications Training Workbook ” , 2000.
- [12]Advantest Corporation, “ Memory Test System Elementary Course Textbook ” , 1994.
- [13]Advantest Corporation, “ Memory Test System Application Course Textbook ” , 1989.
- [14]Advantest Corporation, “ Memory Test System Maintenance Training Textbook ” , 1994.
- [15]Agilent Corporation, “ 93000 SOC Series User Training Textbook ” , 2002.
- [16]Guy Perry, “ The Fundamentals of Digital Semiconductor Testing ” , Soft Test, 1996.
- [17]Robert J.Feugate. JR. and Steven M.Mcintyre, “ Introduction to VLSI Testing ” , Prentice Hall, 1988.
- [18]B. Prince, “ Semiconductor Memories 2nd Ed ” , John Wiley and Sons, 1991.
- [19]M. Quirk, “ Semiconductor Manufacturing Technology ” , Pearson Education PTE. LTD., 2001.
- [20]Xiao, “ Introduction To Semiconductor Manufacturing Technology ” , Pearson Education PTE. LTD., 2000.
- [21]Integrated Service Technology, ” [http://www.isti.com.tw/English/b\\_technology/default.htm](http://www.isti.com.tw/English/b_technology/default.htm) ” ,2004.