

A Study on Space-Time Processing of Downlink DS/CDMA Communication System

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ABSTRACT

This thesis aims to propose a family of downlink space-time processing (DSTP) techniques for downlink DS/CDMA system. In the proposed structure, array of antennas are applied in the basestation and single antenna is applied in each mobile. Thus, downlink channel can be regarded as a multi-input single-output (MISO) system. Spatial processing is realized by a basestation downlink beamformer while temporal processing is performed in each desired mobile. We derive various downlink beamforming weights and temporal processors to meet different criteria such that multiple access interference (MAI) is removed and signal-to-interference-plus-noise-ratio (SINR) is maximized at mobile receiver. We also conduct the analysis in multipath environment and overloaded system (the total paths or the numbers of user have exceeded the array size). The simulation results, demonstrate that the proposed method can eliminate multiple access interference efficiently. Consequently, it enjoys the near-far resistant characteristics. Furthermore, the issue of power assignments for each subscriber is also considered in this thesis. We design the downlink beamforming weights under the zero-forcing criterion, to eliminate MAI. Moreover, the transmitted power assignment for each user should satisfy the minimum received SINR requirements. The allocated power is derived base on underloaded and overloaded situation.

Keywords : DSTP ; MISO ; MAI ; near-far resistant ; Zero-forcing ; Beamforming

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REFERENCES

- [1] B. Sklar, " Digital Communications 2nd ", Chap 11, Prentice Hall, 2001.
 - [2] B. P. Lathi, " Modern Digital and Analog Communication System 3rd ", Chap 9, New York Oxford, 1998.
 - [3] G. Montalbano, D.T.M. Slock, " Matched filter bound optimization for multiuser downlink transmit beamforming ", IEEE International Conference on, Volume: 1, Pages: 677 — 681, Oct. 1998.
 - [4] Theodore S. Rappaport, " Wireless Communication Principles and Practice 2nd ", Chap 5 Prentice Hall, 2002.
 - [5] S. Verdú, " Multiuser Detection ", Cambridge, 1998.
 - [6] W.C. Wu, " Linear Multiuser Detection and Parameter Estimation for Wireless DS/CDMA Communication System ", PhD dissertation, National Tsing-Hua University, Taiwan, ROC, 1998.
 - [7] R. Schmidt, " Multiple emitter location and signal parameter estimation ", IEEE Transactions on Antennas and Propagation, Vol. AP-34: 276-290, March 1986.
 - [8] E. G. Strom, S. Parkvall, S. L. Miller and B. E. Ottersten, " Propagation delay estimation in asynchronous direct-sequence code division multiple access system ", IEEE Trans. Commun. , Vol. 44 No. 1, Pages: 84-93, Jan. 1996.
 - [9] T. Nishimura; T. Ohgane, Y. Ogawa, Y. Doi, J. Kitakado, " Downlink beamforming performance for an SDMA terminal with joint detection ", IEEE Vehicular Technology Conference, Volume: 3, 7-11 Oct. 2001.
 - [10] R. Stridh, M. Bengtsson, B. Ottersten, " System evaluation of optimal downlink beamforming in wireless communication ", IEEE Vehicular Technology Conference, Volume: 1, 2001 [11] M. Schubert, H. Boche, " An efficient algorithm for optimum joint downlink beamforming and power control ", IEEE 55th Vehicular Technology Conference, Volume: 4, 6-9, Pages: 1911-1915, May 2002.
 - [12] H. Boche, M. Schubert, " A new approach to power adjustment for spatial covariance based downlink beamforming ", IEEE International Conference on, Volume: 5, 7-11 May 2001.
 - [13] A. J. Paulraj, C. B. Papadias, " Space-time processing for wireless communications ", Signal Processing Magazine, IEEE, Volume: 14, Issue: 6, Nov. 1997 Pages: 49—83.
 - [14] H. V. Poor, W. Xiaodong, " Space-time Multiuser Detection in Multipath CDMA Channels ", IEEE Transactions on Signal Processing, Volume: 47, Issue: 9, Sept. 1999, Pages: 2356—2374.
 - [15] H. V. Poor, W. Xiaodong, " Blind Adaptive Space-time Multiuser Detection in Multipath CDMA Channels ", Wireless Communications and Networking Conference, 1999. WCNC. 1999 IEEE , 21-24 Sept. 1999 Pages:1033 - 1037 vol.3 [16] Yan Zhou, F. Chin, Ying-Chang Liang, Chi-Chung Ko, " Performance comparison of transmit diversity and beamforming for the downlink of DS-CDMA system ", IEEE Transactions on Wireless Communications, Volume: 2, Issue: 2 , Pages:320 — 334, March 2003 [17] E. Visotsky, U. Madhow, " optimum beamforming using transmit antenna arrays ", IEEE 49th Vehicular Technology Conference, Volume: 1, 16-20, Pages: 851 — 856, May 1999.
 - [18] R. L. Peterson, R. E. Ziemer, D. E. Borth, " Introduction To Spread-Spectrum Communications ", Chap 2, Prentice Hall, 1995.
 - [19] J. G. Proakis, Masoud Salehi, " Communication system engineering ", Chap 11, Prentice Hall, 1994.
 - [20] S. Hakim, " Communication systems ", Chap 7, Wiley, 1994.
 - [21] L. Harte, M. Hoenig, D. McLaughlin, R. K. Kta, " CDMA IS-95 for Cellular and PCS Technology, Economics and Services ", McGraw-Hill, 1999.
 - [22] J. C. Liberti, Jr., T. S. Rappaport, " Smart Antenna for Wireless Communications:IS-95 and Third Generation CDMA Applications ", Prentice Hall PTR, 1999.
 - [23] W. C. Wu, H. W. Chen, " A Family of Linear Downlink Space-Time Processors ", Second Conference on Communication Application, KWIT, Taipei, ROC, Conference Record, Volume 1, Pages: 67—71, 2004.
 - [24] R.A. Horn, and C. R. Johnson, " Matrix Analysis ", Chap. 4, Cambridge University Press 1985.
 - [25] H. Liu, " Signal processing applications in CDMA communications ", Chap 2, Artech House, 2000.
 - [26] W. C. Wu, H. W. Chen, " Downlink Space-Time Processing of DS/CDMA Multipath Signals ", will be publish in International Journal of Electrical Engineering.
 - [27] G. Strang, " Introduction to Linear Algebra ", Chap 4, Wellesley-Cambridge Press, 1993.
 - [28] J. M. Mendel, " Lessons in Estimation Theory for Signal Processing, Communications, and Control ", Chap 3, Englewood Cliffs, NJ: Prentice Hall, 1995.

[29] R. Luples, S. Verdu, " Near-far resistance of multiuser detectors in asynchronous channels ", IEEE Trans. Commun. , Vol. COM-38, Mar 1990.

[30] W. C. Wu, H. W. Chen, " A Simplified Downlink Beamforming and Power Assignment Scheme for Multiuser Wireless Communication System ", ANTEM 2004/URSI, Paper Number: 2004-116