

DS-CDMA系統在無線衰變通道的通道均衡研究

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摘要

在本論文中針對直接序列劃碼多存取系統(DS-CDMA)系統，以系統模擬方式，探討其使用前置循環碼(Cyclic Prefix)，並配合通道均衡器與本文所提出的耙式均衡器(RAKE Equalizer)後之系統效能，並與單純使用耙式接收機(RAKE receiver)的效能相比較。考慮兩種通道，有多路徑無衰變通道、雷利衰落(Rayleigh fading)多重路徑通道。由模擬結果，我們發現在未加入前置循環碼而只使用均衡器來消除切片(chip)與字符(code sequence)間的干擾的情況下，因字符間之干擾嚴重，使得均衡器的效果有限。當加入前置循環碼後，字符間的干擾被移除了，使得均衡器的效能大幅提升。在Rayleigh衰落通道下，雖然字符間的干擾可以有效移除，但是依然無法彌補通道衰落問題，使得其效果比單純的RAKE接收機差。為了要消除切片與字符間的干擾又可提供分集(diversity)的效果，我們提出了耙式均衡器，比單純的RAKE接收機以及均衡器系統效能，我們發現耙式均衡器有更好的效能，在處理增益分別為16和8時，效能分別改善了2.3dB及1.7dB。

關鍵詞：直接序列劃碼多存取；耙式接收機；均衡器；前置循環碼；耙式均衡器

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