Linux

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IPv4   IPv6
IPv4   IPv6   IPv4   IETF
                                                                    (NAT-PT)   Windows   FreeBSD   Linux
(Residential Gateway)   (NAT-PT)

NAT-PT   NAT-PT

:   LINUX   IPv6   NAT-PT

[1]. S. Deering and R. Hinden, " Internet Protocol, Version 6 (IPv6) Specification," RFC 2460, 1998.

[2]. R. Gilligan and E. Nordmark, " Transition Mechanisms for IPv6 Hosts and Routers," RFC 1933, 1996.

[3]. R. Gilligan and E. Nordmark, " Transition Mechanisms for IPv6 Hosts and Routers," RFC 2893, 2000.

[4]. Wen-Tsong Shiue and Chaitali Chakrabarti , " Memory exploration for low power embedded systems," ISCAS '99, Volume: 1 ,pp.

[5]. Hiroyuki Tomiyama, Tohru Ishihara, Akihiko Inoue, and Hiroto Yasuura, " Instruction scheduling for power reduction in processor- based system design," Design, Automation and Test in Europe, 1998., Proceedings , pp. 855- 860, 1998.

[6]. Patrick Hicks, Matthew Walnock, and Robert Michael Owens, " Analysis of power consumption in memory hierarchies," Low Power Electronics and Design, 1997. Proceedings., 1997 International Symposium on , pp. 239-242, 1997.

[7]. G. Tsirtsis and P. Srisuresh, " Network Address Translation- Protocol Translation (NAT-PT) ," RFC 2766 , 2000.

[8]. E. Nordmark., " Stateless IP/ICMP Translation Algorithm (SIIT) ," [9]. Xiaoyu Zhao and Yan Ma, " Linux Based NAT-PT Gateway Implementation," Info-tech and Info-net, 2001. Proceedings. ICII 2001 - Beijing. 2001 International Conferences on , Volume: 5 , pp. 258-263, 2001.

[10]. J.C.B. Mattos, M. Kreutz, and L. Carro, " Low-power control architecture for embedded processors," Integrated Circuits and Systems Design, 2002. Proceedings. 15th Symposium on, pp. 221- 226, 2002.

[11]. David B.Stewart, " Measuring Execution Time and Read-Time Performance," Embedded Systems Conference San Francisco, CA, April 2001.

[12]. http://www.lart.tudelft.nl/lartware/compile-tools [13]. http://gcc.gnu.org/onlinedocs/gcc-3.0/gcc_4.html#SEC54 [14].

http://gcc.gnu.org/onlinedocs/gcc/ARM-Options.html [15]. http://www.ailis.de/~k/knowledge/crosscompiling [16]. Wen-Tsong Shiue and Chaitali Chakrabarti, " Memory Exploration for Low Power Embedded Systems," Journal of VLSI Signal Processing, pp.167-178, Nov 2001. [17]. F. Catthoor, S. Wuytack, L. Nachtergaele, A. Vandecappelle, F. Balasa, and E.D. Greef, " Custom Memory Management Methodology-Exploration of Memory Organization for Embedded Multimedia System Design," Kluwer Academic Publishers, 1998. [18]. http://www.ipv6.or.kr [19]. http://www.research.att.com/sw/tools/graphviz -33- [20]. Ellis Horowitz, Sanguthevar Rajasekaran, and Sartaj Sahni, " Computer Algorithms," New York: Computer Science Press, 1998.
[21].   ,   , "   :   C   ",   :   ,   80