

# 智慧型 IGBT 設計之研究

薛億在、陳勝利；陳勛祥

E-mail: 9223481@mail.dyu.edu.tw

## 摘要

絕緣閘極雙極性電晶體，乃是因應功率元件在高頻應用上所開發出來的元件。它利用整合高功率雙極性電晶體 (Power Bipolar Transistor)以及高功率金氧半電晶體 (Power MOSFET)結構的方法，克服兩個功率元件在應用上的缺點，達到了中高功率、頻率上的有效應用。在本論文中，我們利用半導體製程及元件模擬軟體輔助 (TMA軟體：Tsuprem4, Medici) 來設計一個可以達到200V，20A應用的IGBT元件，最後並且加上ESD以及Latch-Up的防護結構。

關鍵詞：絕緣閘極雙極性電晶體；ESD；Latch-Up

## 目錄

第一章 序論	1.1 簡介與研究動機.....1	1.2 論文架構.....2	第二章 IGBT的基本特性	2.1 序言.....3	2.2 基本操作模式.....4	2.2.1 順向偏壓.....4	2.2.2 逆向偏壓.....6	2.2.3 正常操作.....6	2.3 切換狀態.....8	2.4 寄生閘流體的栓鎖效應.....10	2.5 安全操作區.....12	第三章 各種類IGBT介紹	3.1 序言.....13	3.2 傳統型IGBT.....13	3.3 壕溝式閘極 (Trench Gate) IGBT.....15	3.4 橫向 (Lateral) IGBT.....17	3.5 橫向壕溝式閘極 (Lateral Trench Gate) IGBT.....18	3.6 橫向壕溝式電極 (Lateral Trench Electrode) IGBT.....20	第四章 元件設計及量測結果	4.1 序言.....22	4.2 IGBT-TSUPREM4模擬及製程.....23	4.3 Medici特性模擬.....31	4.4 保護電路設計.....34	4.4.1 ESD保護電路設計.....34	4.4.2 終止保護環 (Ring) 設計.....36	4.5 Layout.....38	4.6 量測結果.....40	第五章 結論.....43	附錄一.....44	附錄二.....50	Reference.....54
--------	-------------------	----------------	---------------	--------------	------------------	------------------	------------------	------------------	----------------	-----------------------	------------------	---------------	---------------	--------------------	-------------------------------------	------------------------------	---	--	---------------	---------------	-------------------------------	-----------------------	-------------------	------------------------	------------------------------	-------------------	-----------------	---------------	------------	------------	------------------

## 參考文獻

- [1]S.M.Sze, "Modern Semiconductor Device Physics", A Wiley Interscience publication, 1998.
- [2]B.Jayant, Baliga, "The Future of Power Semiconductor Device Technology", Proc. of the IEEE, pp 822-832, Vol.89, June 2001.
- [3]Hideo Iwamoto, Hideki Haruguchi, Yoshifumi Tomomatsu, John F. Donlon, Eric R. Motto, "A new punch through IGBT having a new N-buffer layer", Proc. of the IEEE, pp 692-699, 1999.
- [4]Yuan Xiaolu, F. Udrea, L.Coulbeck, P.Waind, G. Amaratunga, "On-state analytical modeling of IGBTs with local lifetime control", IEEE Transactions on Power Electronics, pp 815-823, Vol.17, Sep 2002.
- [5]J. Zeng, P.A. Mawby, M.S. Towers, K.Board, "Effect of carrier lifetimes on forward characteristics of MOS-controlled thyristors", Circuits, Devices and Systems, IEE Pro., Vol.142, Jun 1995.
- [6]S.Azzopardi, A.Kawamura, H.Iwamoto, "Switching performances of 1200 V conventional planar and trench punch-through IGBTs for clamped inductive load under extensive measurements", Power Electronics and Motion Control Conference, Vol. 1, 2000.
- [7]H. Sumida, K. Ueno, N. Iwamuro, S. Tagami, "Numerical analysis of switching in the IGBT triggered thyristor", Proc. of the 3rd International Symposium on Power Semiconductor Devices and ICs, pp 132-137, 1991.
- [8]Kang Goo, Moon Seung Hyun, Sung Man Young, "A small sized lateral trench electrode IGBT having improved latch-up and breakdown characteristics for power IC system", Pro. of IEEE, Vol.1, 2001.
- [9]K.Board, Z-R.Hu, "New latch-up-free IGBT with low on-resistance", Electronics Letters, Vol. 29, pp 1664-1666, 2 Sep 1993.
- [10]Naresh Thapar, B.J.Baliga, "A new IGBT structure with a wider safe operating area", Proc. of the 6th International Symposium on Power Semiconductor Devices and ICs, pp 177 -182, 31 May-3 Jun 1994.
- [11]N.Iwamuro, M.S.Shekar, B.J.Baliga, "A study of EST's short-circuit SOA", Proc. of the 5th International Symposium on Power Semiconductor Devices and ICs, pp 71 -76, 18-20 May 1993.
- [12]劉中民, "公元2000年之功率元件技術發展趨勢", 電力電子技術, pp 10-22, 2000.2.
- [13]M.Tanaka, S.Teramae, Y.Takahashi, T.Takeda, M.Yamaguchi, T.Ogura, T.T.sunoda, S.Nakao, "600 V trench-gate NPT-IGBT with excellent low on-state voltage", Power Semiconductor Devices and ICs, pp 279-282, 2000.
- [14]H.-R.Chang, B.J.Baliga, "500V n-channel insulated-gate bipolar transistor with a trench gate structure", IEEE Transactions on Electron

Devices, Vol. 36, pp 1824 -1829, 9 Sep 1989.

[15]T.Takeda, M.Kuwahara, S.Kamata, T.Tsunoda, K.Imamura, S.Nakao, " 1200 V trench gate NPT-IGBT (IEGT) with excellent low on-state voltage " , Proc. of the 10th International Symposium on Power Semiconductor Devices and ICs, pp 75 -79, 3-6 Jun 1998.

[16]O.Spulber, E.M.S.Narayanan, S.Hardikar, M.M De Souza, M. Sweet, J.V Bose , C.S., " A novel gate geometry for the IGBT: the trench planar insulated gate bipolar transistor (TPIGBT) " , IEEE Electron Device Letters , Vol.20, pp 580 -582, Nov 1999.

[17]Y.Onishi, S.Momota, Y.Kondo, M.Otsuki, N.Kumagai, K.Sakurai, " Analysis on device structures for next generation IGBT " , Pro. of the 10th International Symposium on Power Semiconductor Devices and ICs, pp 85 -88, Jun 1998.

[18]何繼勛, " 高壓元件Termination之模擬與設計 " , 電力電子技術, pp 59-67, 2000.2.

[19]張俊彥譯, 施敏原著, " 半導體元件物理與製作技術 " , 高立圖書有限公司, 1996.

[20]Donald A. Neamen著, 李世鴻、陳勝利譯, " 半導體元件物理 " , 台商圖書, 1998.