

金屬誘發成長低溫多晶矽之特性研究

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摘要

本論文係研究以金屬-鎳(Ni)來誘發含氫之非晶矽薄膜，此進行之方式是採取成核與成長同時進行來形成多晶矽之技術。此技術是利用快速升溫化學氣相沉積系統成長矽薄膜與鎳直接進行反應，並針對不同厚度比例之鎳來探討結晶性。再者也研究如何改善金屬污染以及提升多晶矽薄膜特性。從目前研究發現，成長矽溫度550 以及氫含量控制在30sccm、金屬厚度為20nm時，所獲得結晶性最好。此外，考量到金屬污染的因素，除了減低金屬厚度至5nm之外，還選用特定比例溶液5HNO₃ : 1HCl以及蝕刻時間約三分鐘，來減低金屬殘留於矽薄膜中之含量達到2.6%。最後吾人利用約數小時退火可以發現，單一晶粒的大小可達到數百nm，並且片電阻可低至100 /sq.以下。

關鍵詞：多晶矽；快速升溫化學氣相沉積；金屬污染

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參考文獻

- Reference [1] Akio Mimura, Nobutake Konishi, and Kikuo Ono, " High Performance Low temperature Poly-Si n-Channel TFT ' s for LCD, " IEEE Trans. Electron Devices, vol. 36, p. 351, 1989.
- [2] T. Serikawa, Seiiti Shirai, Akio Okamoto, and Shiro Suyama, " Low-Temperature Fabrication of High-Mobility Poly-Si TFT ' s for Large-Area LCD ' s, " IEEE Trans. Electron Devices, vol. 36, p.1929, 1989.
- [3] S.D. Brotherton, J.R. Ayres, and M.J. Edwards, " Laser crystallization poly-Si TFTs for AMLCDs, " Thin Solid Films, 337(1999) 185-195.
- [4] K. Sera et al., " High temperature TFT ' s Fabricated by XeCl Excimer Laser Annealing of Hydrogenated Amorphous-Silicon Film, " IEEE Trans. Electron Devices, vol. 36, p. 2868, 1989.
- [5] Miltiadis K. Hatalis, and David W. Greve, " High-Performance Thin Film Transistors in Low-Temperature Crystallized LPCVD Amorphous Silicon Films, " IEEE Electron Devices, vol. EDL-8, p. 361, 1987.
- [6] T.Aoyama, G. Kawachi, and N. Konishi, " Crystallization of LPCVD Silicon Films by Low Temperature Annealing, " J. Electrochem. Soc.,

vol. 136, p.1169, 1989.

- [7] R. Kakkad, J. Smith, and W.S. Lau, "Crystallize Si films by low-temperature rapid thermal annealing of amorphous silicon," *J. Appl. Phys.*, 65(5), p.2069, 1989.
- [8] L. Haji, and P. Joubert, "Mode of growth and Microstructure of polycrystalline silicon obtained by solid-phase crystallization of an amorphous silicon film," *J. Appl. Phys.*, 75(8), p.3944, 1994.
- [9] Kee-Soo Nam, Yoon-Ho Song, and Jong-Tae Baek, "Thin-Film Transistors with Polycrystalline Silicon Prepared by a New Annealing Method," *Jpn. J. Appl. Phys.*, vol. 32, p.1908, 1993.
- [10] M. Bonnel, N. Duhamel, and L. Haji, "Polycrystalline Silicon Thin-Film Transistors with Two-Step Annealing Process," *IEEE Electron Device Letters*, vol. 14, p.551, 1993.
- [11] V. Subramanian, Paul Dankoski, Butrus and Levent Degertekin, "Controlled Two-Step Solid-Phase Crystallization for High-Performance Polysilicon TFT's," *IEEE Electron Device Letters*, vol. 18, p. 378, 1997.
- [12] Huang-Chung Cheng, Chun-Yao Huang, and Fang-Shing Wang, "Thin-Film Transistors with Polycrystalline Silicon Films Prepared by Two-Step Rapid Thermal Annealing," *Jpn. J. Appl. Phys.*, vol. 39, p. L19, 1999.
- [13] Jae-Hong Jeon, Min-Cheol Lee, Kee-Chan Park, and Min-Koo Han, "A New Polycrystalline Silicon TFT with a Single Grain Boundary in the Channel," *IEEE Electron Device Letters*, vol. 22, p. 429, 2001.
- [14] Min-Cheol Lee, Sang-Hoon Jung, In-Hyuk Song, and Min-Koo Han, "A New Poly-Si TFT Structure With Air Cavities at the Gate-Oxide Edges," *IEEE Electron Device Letters*, vol. 22, p. 539, 2001.
- [15] Ching-Wei Lin, Li-Jing Cheng, Yin-Lung Lu, Yih-Shing Lee, and Huang-Chung Cheng, "High performance Low-Temperature Poly-Si TFTs Crystallized by Excimer Laser Irradiation with Recessed-Channel Structure," *IEEE Electron Device Letters*, vol. 22, p. 269, 2001.
- [16] Ching-Wei Lin, Chang-Ho Tseng, Ting-Kuo Chang, Chiung-Wei Lin, Wen-Tung, and Huang-Chung Cheng, "A Novel Laser-Processed Self-Aligned Gate-Overlapped LDD Poly-Si TFT," *IEEE Electron Device Letters*, vol. 23, p. 133, 2002.
- [17] Ryoichi Ishihara, and Masakiyo Matsumura, "Excimer Laser Produced Single-Crystal Silicon Thin-Film Transistors," *Jpn. J. Appl. Phys.*, vol. 36, p. 6167, 1997.
- [18] H. Kuriyama, T. Nohda, Y. Aya, T. Kuwahara, K. Wakisaks, S. Kiyama, and S. Tsuda, "Comprehensive Study of Lateral Grain Growth in Poly-Si Films by Excimer Laser Annealing and Its Application to Thin Film Transistors," *Jpn. J. Appl. Phys.*, vol. 33, p.5657, 1994.
- [19] T. Sameshima, M. Hara, and S. Usui, "XeCl Excimer Laser Annealing Used to Fabricate Poly-Si TFT's," *Jpn. J. Appl. Phys.*, vol. 28, p. 1789, 1989.
- [20] P. Mei, J. B. Boyce, M. Hack, R. A. Lujan, R. I. Johnson, G. B. Anderson, D. K. Fork, and S. E. Ready, *Appl. Phys. Lett.*, vol. 64, p. 1132, 1994.
- [21] K. Masumo, M. Kunigita, S. Takafuji, and M. Yuki, *J. Non-Cryst. Solids* 115, 147 (1989).
- [22] H. Zhang, N. Kusumoto, T. Inushima, and S. Yamazki, *IEEE Electron Device Letters*, vol. 13, p. 297, 1992.
- [23] G. K. Giust, T. W. Sigmon, P. G. Carey, B. Weiss, and G. A. Davis, *IEEE Electron Device Letter*, vol. 19, p. 343, 1998.
- [24] K. H. Lee, J. K. Park, and J. Jang, *IEEE Trans. Electron Devices*, vol. 45, p. 2548, 1998.
- [25] K. Kitahara, A. Hara, K. Nakajima, and M. Okabe, "Silicon-Hydrogen Bonds in Laser-Crystallization Polysilicon Thin Films and Their Effects on Electron Mobility," *Jpn. J. Appl. Phys.*, vol. 38, p. 1320, 1999.
- [26] N. H. Nickel, N. M. Johnson, and W. B. Jackson, *Appl. Phys. Lett.*, vol. 62, p. 3285, 1993.
- [27] H. Kuriyama et al., "Enlargement of Poly-Si Film Grain Size by Excimer Laser Annealing and Its Application to High-Performance Poly-Si Thin Film Transistor," *Jpn. J. Appl. Phys.*, vol. 30, p. 3700, 1991.
- [28] H. Kuriyama, S. Kiyama, T. Kuwahara, S. Noguchi, and S. Nakano, *MRS Bull.* 321(1993)657.
- [29] K. Shimizu, O. Sugiura, and M. Matsumura, *IEEE Tran. Electron Devices*, vol. 40 p. 112, (1993).
- [30] R. Ishihara, W. C. Yeh, T. Hattori, and M. Matsumura, "Effects of Light Pulse Duration on Excimer Laser Crystallization Characteristics of Silicon Thin Films," *Jpn. J. Appl. Phys.*, vol. 34, p. 1759, 1995.
- [31] L. Mariucci, R. Carluccio, A. Pecora, V. Foglietti, G. Fortunato, P. Legagneux, D. Pribat, D. Della Sala, and J. Stoemenos, "Lateral growth control in excimer laser crystallized polysilicon," *Thin Solid Films* 337, 137(1999).
- [32] R. Ishihara, and M. Matsumura, *Electr. Lett.* 31, 1956(1995) [33] D. H. Choi, E. Sadayuki, O. Sugiura, and M. Matsumura, "Lateral Growth of Poly-Si Film by Excimer Laser and Its Thin Film Transistor Application," *Jpn. J. Appl. Phys.*, vol. 33, p. 70, 1994.
- [34] D. H. Choi, K. Shimizu, Osamu Sugiura and M. Matsumura, "Drastic Enlargement of Grain Size of Excimer-Laser-Crystallized Polysilicon Films," *Jpn. J. Appl. Phys.*, vol. 31, p. 4545, 1992.
- [35] H. J. Kim, and J. S. Im, *Mat. Res. Soc. Symp. Proc.* 358, 903 (1995) [36] H. Kuriyama et al., "Improving the Uniformity of Poly-Si Films Using a New Excimer Laser Annealing Method for Giant-Microelectronics," *Jpn. J. Appl. Phys.*, vol. 31, p. 4550, 1992.
- [37] R. C. Cammarata and C. V. Thompson, "NiSi₂ precipitation in nickel-implanted silicon films," *Appl. Phys. Lett.* 51(14), p. 1106, 1987.
- [38] C. Hayzelden, J. L. Batstone, and R. C. Cammarata, "In situ transmission electron microscopy studies of silicide-mediated crystallization of amorphous silicon," *Appl. Phys. Lett.* 60(2), p. 225, 1992.

- [39] C. Hayzelden, and J. L. Batstone, "Silicide formation and silicide-mediated crystallization of nickel-implanted amorphous silicon thin films," *J. Appl. Phys.*, vol. 73, p. 8279, 1993.
- [40] H. L. Gaigher, and N. G. Van Der Berg, "The Structure of Gold Silicide in thin Au/Si films," *Thin Solid Films* 68, p. 373, 1980.
- [41] By B. Y. Tsauro, and J. W. Mayer, "Metastable Au-Si alloy formation induced by ion-beam interface mixing," *Philosophical Magazine A*, vol. 43, p. 345, 1981.
- [42] L. Hultman, A. Robersson, and H. T. G. Hentzell, "Crystallization of amorphous silicon during thin-film gold reaction," *J. Appl. Phys.* 62(9), p.3647, 1987.
- [43] G. Radnoczi, A. Robersson, H. T. G. Hentzell, S. F. Gong, and M. A. Hasan, "Al induced crystallization of a-Si," *J. Appl. Phys.* 69(9), p. 6394, 1991.
- [44] M. S. Haque, H. A. Naseem, and W. D. Brown, "Interaction of aluminum with hydrogenated amorphous silicon at low temperature," *J. Appl. Phys.* 75(8), p.3928, 1994.
- [45] M. S. Ashtikar, and G. L. Sharma, "Silicide mediated low temperature crystallization of hydrogenated amorphous silicon in contact with aluminum," *J. Appl. Phys.* 78(2), p.913, 1995.
- [46] K. Nakamura, J. O. Olowolafa, S. S. Lau, M-A. Nicolet, and J. W. Mayer, "Interaction of metal layers with polycrystalline Si," *J. Appl. Phys.*, vol. 47, p.1278, 1976.
- [47] S. W. Russell, Jian Li, and J. W. Mayer, "In situ observation of fraction growth during a-Si crystallization in a Cu₃Si matrix," *J. Appl. Phys.* 70(9), p.5153, 1991.
- [48] B. Bian, J. Yie, B. Li, and Z. Wu, "Fractal formation in a-Si:H/Ag/a-Si:H films after annealing," *J. Appl. Phys.* 73(11), p.7402, 1993.
- [49] R. J. Nemanich, R. T. Fulks, B. L. Stafford, and H. A. Vander Plas, "Initial reactions and silicide formation of titanium on silicide studied by Raman spectroscopy," *J. Vac. Sci. Technol.* A3(3), p. 938, 1985.
- [50] E. Nygren, A. P. Pogany, K. T. Short, and J. S. Williams, "Impurity-stimulated crystallization and diffusion in amorphous silicon," *Appl. Phys. Lett.* 55(6), p.439, 1988.
- [51] S. W. Lee, Y. C. Jeon, and S. K. Joo, "Pd induced lateral crystallization of amorphous Si thin films," *Appl. Phys. Lett.* 66(13), p. 1671, 1995.
- [52] F. d'Heurle, S. Petersson, and L. Stolt, and B. Strizker, "Diffusion in intermetallic compounds with the CaF₂ Structure: A marker study of the formation of NiSi₂ thin films," *J. Appl. Phys.*, vol. 53, p. 5678, 1982.
- [53] Y. Kawazu, H. Kudo, S. Onari, and T. Arai, "Initial Stage of the Interfacial Reaction between Nickel and Hydrogenated Amorphous Silicon," *Jap. J. Appl. Phys.*, vol. 29, p.729, 1990.
- [54] T. J. Konno, and R. Sinclair, "Metal-Induced crystallization of Semiconductors," *Materials Science and Engineering.* A179/A180, p. 426, 1994.
- [55] S. Y. Yoon, K. H. Kim, and C. O. Kim, "Low temperature metal induced crystallization of amorphous silicon using a Ni solution," *J. Appl. Phys.* 82(11), p.5865, 1997.
- [56] Jin Jane, J. Y. Oh, S. K. Kim, Y. J. Choi, S. Y. Yoon, and C. O. Kim, "Electric-field-enhanced crystallization of amorphous silicon," *Nature*, vol. 395, p. 481, 1998.
- [57] S. Y. Yoon, J. Y. Oh, C. O. Kim, and J Jang, "Low temperature solid phase crystallization of amorphous silicon at 380 °C," *J. Appl. Phys.*, vol. 84, p. 6463, 1998.
- [58] K. H. Lee, Y. K. Fang, and S. H. Fan, "Au metal-induced lateral crystallization (MILC) of hydrogenated amorphous silicon thin film with very low annealing temperature and fast MILC rate," *Electronics Lett.*, vol. 35, p. 1108, 1999.
- [59] S. W. Lee, and S. K. Joo, "Low Temperature Poly-Si Thin-Film Transistor Fabricated by Metal-Induced Lateral Crystallization," *IEEE Electron Device Letter*, vol. 17, p. 160, 1996.
- [60] S. W. Lee, and T. H. Ihn, and S. K. Joo, "Fabrication of High-Mobility p-Channel Poly-Si Thin-Film Transistors by Self-Aligned Metal-Induced Lateral Crystallization," *IEEE Electron Device Letter*, vol. 17, p. 407, 1996.
- [61] Z. Jin, G. A. Bhat, M. Yeung, H. S. Kwok, and M. Wong, "Nickel induced crystallization of amorphous silicon thin films," *J. Appl. Phys.*, vol. 84, p. 194, 1998.
- [62] Z. Jin, K. Moulding, H. S. Kwok, and M. Wong, "The effects of Extended Heat Treatment on Ni Induced Lateral Crystallization of Amorphous Silicon Thin Film," *IEEE Trans. Electron Devices*, vol. 46, p. 78, 1999.
- [63] G. A. Bhat, H. S. Kwok, and M. Wong, "Behavior of the drain current in metal-induced lateral crystallized thin film transistors," *Solid-State Electronics* 44(2000), p.1321.
- [64] T. K. Kim, G. B. Kim, B. I. Lee, and S. K. Joo, "The effects of Electrical Stress and Temperature on the properties of Polycrystalline Silicon Thin-Film Transistors Fabricated by Metal Induced Lateral Crystallization," *IEEE Electron Device Letters*, vol. 21, p. 347, 2000.
- [65] E. A. Guliants, W. A. Anderson, L. P. Guo, and V. V. Guliants, "Transmission electron microscopy study of Ni silicides formed during metal-induced silicon growth," *Thin Solid Films* 385(2001), p. 74.
- [66] E. A. Guliants, C. Ji, Y. J. Song, and W. A. Anderson, "A 0.5- μ m-thick polycrystalline silicon Schottky diode with rectification ratio of 106,

" Appl. Phys. Lett., vol. 80, p. 1474, 2002.

[67] E. A. Gulians and W. A. Anderson, " Metal-Induced Growth of Poly-Si on foreign substrates for Solar cell application, " J. Appl. Phys., vol. 87, p. 3535, 2000.

[68] B. D. Cullity, " Elements of X-ray diffraction, " Addison-Wesley Pub. Co, Inc., 1978.

[69] D. J. Gardiner, and P. R. Graves, Ed. Practical Raman Spectroscopy, Springer-Verlag, Berlin, (1989).

[70] D. E. Newbury, D. C. Joy, P. Echlin, C. E. Fiori, and J. I. Goldstein, " Advanced Scanning Electron Microscopy and X-Ray Microanalysis, " Plenum Press, New York (1986).

[71] R. Wiesendanger, " Scanning Probe Microscopy and Spectroscopy: Methods and Applications, " Cambridge University Press, Cambridge, 1994.

[72] M. Thommpson, M. D. Barker, A. Christie and J. F. Tyson, " Auger Electron Spectroscopy, " John Wiley & Sons, New York (1985).