

APPLYING OBJECT-ORIENTED TECHNOLOGY AND COMPUTATIONAL INTELLIGENCE TO CONSTRUCT AN OPTIMIZING QUALITY CONTROLLER FOR SEM

黃寶賢、葉進儀

E-mail: 9127945@mail.dyu.edu.tw

ABSTRACT

This research presents a run-to-run(R2R) multiple input-multiple output controller for semiconductor manufacturing processes. The controller, termed Computational Intelligence Quality Controller (CIQC), can act as a controller for give models. Recursive estimation techniques are utilized for the on-line estimation of the parameters of a MIMO model of the Hammerstein type. Real-Valued Genetic Algorithm (RVGA) is used to obtain the optimal solution for the next run. The genetic operations, inspired by Darwin's theory about evolution, include selection, reproduction, crossover, and mutation. An Unified Modeling Language (UML) is applied in this research for the system analysis and design of the R2R controller. The software implementation of the CIQC uses the MATLAB because it owns a lot of useful toolboxes and can provide a simple and friendly user interface. For testing purposes, Chemical Mechanical Planarization(CMP) processes were simulated based on real equipment models provided by SEMATECH. It is shown that under the presence of noise and drift disturbances of different magnitudes, the CIQC allows to keep adequate control of the responses even if the input-output transfer function is severely nonlinear, and can offer better performance than the Optimizing Adaptive Quality Controller(OAQC).

Keywords : Run-to-Run Controller ; UML ; Real-Valued Genetic Algorithm ; Chemical Mechanical Planarization

Table of Contents

第一章緒論--P1 1.1 研究背景--P1 1.2 研究動機--P2 1.3 研究目的--P4 1.4 論文架構--P4 第二章文獻回顧--P6 2.1 SPC/EPC 的整合探討--P6 2.2 製程最佳化與控制--P7 2.3 R2R 控制--P8 2.4 R2R 控制器應用於半導體製程--P10 2.5 智慧型計算--P12 第三章研究方法--P16 3.1 自調適控制於R2R製程--P16 3.1.1 製程模式--P16 3.1.2 控制行為--P18 3.1.3 遞迴最小平方演算法--P20 3.2 智慧型計算品質控制器--P21 3.3 系統識別對象的建立--P22 3.4 多變量遞迴最小平方演算法--P24 3.5 非線性最佳化--P25 3.5.1 參數設定--P27 3.5.2 創造初始母體--P29 3.5.3 計算適應函數與適應值--P29 3.5.4 複製--P30 3.5.5 交配--P32 3.5.6 突變--P33 3.5.7 取代--P33 3.5.8 終止條件--P34 3.6 實驗設計--P34 3.7 物件導向分析與設計--P35 3.7.1 統一模式語言--P35 3.7.2 需求階段--P36 3.7.3 分析與設計階段--P42 3.7.4 實作階段--P52 3.7.5 測試階段--P52 3.8 系統與方法的整合--P52 第四章模擬結果與分析--P54 4.1 化學機械研磨製程--P54 4.2 效能衡量--P55 4.3 參數選定--P56 4.4 比較分析--P57 4.4.1 製程控制分析 - 文獻方面--P57 4.4.2 製程控制分析 - 實務方面--P61 第五章結論與建議--P68 5.1 結論--P68 5.2 建議與未來研究方向--P69 參考文獻--P71 附錄A--P76

REFERENCES

- [1] 張裕益譯, 「UML 使用手冊」, 博碩文化股份有限公司, 2001。
- [2] 楊正甫著, 「物件導向分析與設計」, 松崗電腦圖書, 2000。
- [3] 吳仁和, 林信惠著, 「系統分析與設計」, 智勝文化事業有限公司, 2001。
- [4] 江行全, 范書愷, 任志宏, 王建智, 「多變量自適應控制應用於半導體R2R 製程」, 中國工業工程學會九十年年度年會暨學術研討會, 高雄, 2001。
- [5] ASTROM, K. J AND WITTENMARK, B., "ON SELF TUNING REGULATORS", AUTOMATICA, VOL.9,1973, PP.185-199.
- [6] BONING, D.S.; MOZUMDER, P.K., "DOE/OPT: A SYSTEM FOR DESIGN OF EXPERIMENTS, RESPONSE SURFACE MODELING, AND OPTIMIZATION USING PROCESS AND DEVICE SIMULATION," IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 7, ISSUE 2, 1994, PP.233-244.
- [7] BONING, D.; MOYNE, W.; SMITH, T.; MOYNE, J.; HURWITZ, A., "PRACTICAL ISSUES IN RUN BY RUN PROCESS CONTROL," ASMC 95 PROCEEDINGS, 1995, PP.201-208.
- [8] BOX, G. E. P. AND KRAMER, T., "STATISTICAL PROCESS MONITORING AND FEEDBACK ADJUSTMENT A DISCUSSION," TECHNOMETRICS, VOL. 34, NO.3, 1992, PP.251-267.
- [9] BUTLER, S.W.; STEFANI, J.A., "SUPERVISORY RUN-TO-RUN CONTROL OF A POLYSILICON GATE ET -CH USING IN SITU

- ELLIPSOMETRY," IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 7, ISSUE 2, 1994, PP.193-201.
- [10] CLARKE, D. W. AND GAWTHROP, P. J., "SELF TUNING CONTROLLER," PROC.IEE, VOL.122, NO.9, 1975, PP.929-935.
- [11] DEL CASTILLO, E., "A MULTIVARIATE SELF-TUNING CONTROLLER FOR RUN-TO-RUN PROCESS CONTROL UNDER SHIFT AND TREND DISTURBANCES," IIE TRANSACTION, VOL. 28, NO. 12, 1996, PP.1011 -1021.
- [12] DEL CASTILLO, E. AND HURWITZ, A.M., "RUN TO RUN PROCESS: A LITERATURE REVIEW AND SOME EXTENSIONS," JOURNAL OF QUALITY TECHNOLOGY, VOL. 29, NO. 2, 1997, PP.184-196.
- [13] DEL CASTILLO, E.; JINN-YI YEH, "AN ADAPTIVE RUN-TO-RUN OPTIMIZING CONTROLLER FOR LINEAR AND NONLINEAR SEMICONDUCTOR PROCESSES," IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 11, ISSUE 2, 1998, PP.285-295.
- [14] EDGAR, T.F.; CAMPBELL, W.J.; BODE, C., "MODEL-BASED CONTROL IN MICROELECTRONICS MANUFACTURING," IEEE CONFERENCE ON DECISION AND CONTROL, VOL. 4, 1999, PP.4185-4191.
- [15] GASTON, G.J.; WALTON, A.J., "THE INTEGRATION OF SIMULATION AND RESPONSE SURFACE METHODOLOGY FOR THE OPTIMIZATION OF IC PROCESSES," IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 7, ISSUE 1, 1994, PP.22-33.
- [16] GOLDBERG, D.E., "GENETIC ALGORITHMS IN SEARCH, OPTIMIZATION, AND MACHINE LEARNING", ADDISON-WESLEY, READING, MASS, 1989.
- [17] HANKINSON, M.; VINCENT, T.; IRANI, K.B.; KHARGONEKAR, P.P., "INTEGRATED REAL-TIME AND RUN-TO-RUN CONTROL OF ETCH DEPTH IN REACTIVE ION ETCHING," IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 10, ISSUE 1, 1997, PP.121-130.
- [18] HAUPT R.L AND HAUPT S.E., PRACTICAL GENETIC ALGORITHMS, WILEY INTERSCIENCE PUBLICATION, 1998.
- [19] HU, A.; SACHS, E.; INGOLFSSON, A.; LANGER, P., "RUN-BY-RUN PROCESS CONTROL: PERFORMANCE BENCHMARKS," IEEE/SEMI INTERNATIONAL SEMICONDUCTOR MANUFACTURING SCIENCE SYMPOSIUM, 1992, PP.73-78.
- [20] HU, A.; HE DU; WONG, S.; RENTELN, P.; SACHS, E., "APPLICATION OF RUN BY RUN CONTROLLER TO THE CHEMICAL-MECHANICAL PLANARIZATION PROCESS," ELECTRONICS MANUFACTURING TECHNOLOGY SYMPOSIUM, VOL. 1, 1994, PP.371-378.
- [21] INGOLFSSON, A. AND SACHA, E., "STABILITY AND SENSITIVITY OF AN EWMA CONTROLLER," JOURNAL OF QUALITY TECHNOLOGY, VOL. 25, NO. 4, 1993, PP.271-287.
- [22] LIMANOND, S.; SI, J.; TSAKALIS, K., "MONITORING AND CONTROL OF SEMICONDUCTOR MANUFACTURING PROCESSES," IEEE CONTROL SYSTEMS MAGAZINE, VOL. 18, ISSUE 6, 1998, PP.46-58.
- [23] MAN, K. F., TANG, K. S., AND KWONG, S., GENETIC ALGORITHMS, SPRINGER, VERLAG, LONDON, 1999.
- [24] MONTGOMERY, D.C., KEATS, J.B., RUNGER, G.C., AND MESSINA, W., "INTEGRATING STATISTICAL PROCESS CONTROL AND ENGINEERING PROCESS CONTROL," JOURNAL OF QUALITY TECHNOLOGY, VOL. 26, NO. 2, 1994, PP.79-87.
- [25] MOYNE, J.R.; TELFEYAN, R.; HUNVITZ, A.; TAYLOR, J., "A PROCESS-INDEPENDENT RUN-TO-RUN CONTROLLER AND ITS APPLICATION TO CHEMICAL-MECHANICAL PLANARIZATION," IEEE/SEMI ADVANCED SEMICONDUCTOR MANUFACTURING CONFERENCE, 1995, PP.194-200.
- [26] MOYNE, J.; DEL CASTILLO, E AND HURWITZ, A. M., "RUN-TO-RUN CONTROL IN SEMICONDUCTOR MANUFACTURING", CRC, BOCA RATON, FLORIDA, 2001.
- [27] MOZUMDER, P.K.; BARNA, G.G., "STATISTICAL FEEDBACK CONTROL OF A PLASMA ETCH PROCESS," IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 7, ISSUE 1, 1994, PP.1-11.
- [28] MOZUMDER, P.K.; SAXENA, S.; COLLINS, D.J., "A MONITOR WAFER-BASED CONTROLLER FOR SEMICONDUCTOR PROCESSES," IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 7, ISSUE 3, 1994, PP.400-411.
- [29] MUSACCHIO, J.; RANGAN, S.; SPANOS, C.; POOLLA, K., "ON THE UTILITY OF RUN TO RUN CONTROL IN SEMICONDUCTOR MANUFACTURING," IEEE INTERNATIONAL SYMPOSIUM ON SEMICONDUCTOR MANUFACTURING CONFERENCE PROCEEDINGS, 1997, PP.9-12.
- [30] RUEY-SHAN GUO; ARGON CHEN; JIN-JUNG CHEN, "RUN-TO-RUN CONTROL SCHEMES FOR CMP PROCESS SUBJECT TO DETERMINISTIC DRIFTS," SEMICONDUCTOR MANUFACTURING TECHNOLOGY WORKSHOP, 2000, PP.251-258.
- [31] SACHS, E.; HU, A.; INGOLFSSON, A., "RUN BY RUN PROCESS CONTROL: COMBINING SPC AND FEEDBACK CONTROL," IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL.8, ISSUE 1, 1995, PP.26-43.
- [32] SCHWEFEL, H.-P., "EVOLVING SOLUTIONS FOR DESIGN AND MANAGEMENT TASKS ON COMPUTERS," IEEE INTERNATIONAL CONFERENCE ON SYSTEMS, MAN, AND CYBERNETICS, 1999, PP.573-578.
- [33] SOVARONG LEANG; SHANG-YI MA; THOMSON, J.; BOMBAY, B.J.; SPANOS, C.J., "A CONTROL SYSTEM FOR PHOTOLITHOGRAPHIC SEQUENCES," IEEE TRANSACTIONS ON SEMICONDUCTOR MANUFACTURING, VOL. 9, ISSUE 2, 1996, PP.191-207.

[34] TAGUCHI, G., INTRODUCTION TO QUALITY ENGINEERING, ASIAN PRODUCTIVITY ORGANIZATION, UNI -PUB, WHITE PLAINS, NY, 1986.