

# Low Power Laser Diode Driver Circuit Design

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## ABSTRACT

In this thesis, we present two low-power laser diode drivers (Circuit I, and Circuit II), using DC or AC coupling interface circuit to connect it with laser diode. The TSMC 0.35 m 1P4M technology parameters are used to simulate our design. The simulation data rate is 1.25Gbps. For Circuit I, both DC and AC coupling interface circuits are used and can work at 1.25Gbps data rate. For Circuit II, using DC coupling interface circuit will have “ headroom ” problem, so that the 1.25Gbps data rate cannot be achieved. However, using AC coupling interface circuit to construct Circuit II can work at 1.25Gbps and has less power consumption.

Keywords : laser diode driver ; low power ; optical-fiber communication

## Table of Contents

|   |     |   |      |
|---|-----|---|------|
| 封面內頁 簽名頁 授權書.....   | iii | 中文摘要.....                               | iii  |
| .....iv 英文摘要.....   | iv  | v 誌謝.....                               | v    |
| .....vi 目錄.....   | vi  | viii 圖目錄.....                           | viii |
| .....x 表目錄.....   | x   | xiii 第一章 緒論 1.1                         | xiii |
| 光纖通訊系統.....   | 1   | 1.1.1 基本原理.....                         | 2    |
| .....3 1.1.3 光源.....  | 3   | 1.1.2 應用.....                           | 2    |
| .....6 1.2.1 功率消耗.....  | 6   | 1.2 低功率設計.....                          | 4    |
| .....9 1.2.2 研究動機.....  | 9   | 1.2.3 低功率技術.....                        | 9    |
| .....9 第二章 雷射二極體 2.1 特性.....  | 15  | 2.2 驅動電路.....                           | 15   |
| .....17 2.2.1 PECL to CMOS Logic Circuit.....                               | 17  | 2.2.2 Modulation Current Generator..... | 17   |
| .....18 2.2.3 Bias Current Generator.....                                   | 19  | 2.3 Eye diagram .....                   | 20   |
| 第三章 Laser driver with DC coupling 3.1 NCULDD.....                           | 24  | 3.1.1 電路架構.....                         | 24   |
| .....24 3.1.2 模擬結果.....   | 27  | 3.2 電路一.....                            | 29   |
| 電路架構.....   | 29  | 3.2.1 電路架構.....                         | 29   |
| .....39 3.2.2 模擬結果.....   | 37  | 3.3 電路二.....                            | 37   |
| .....44 第四章 Laser driver with AC coupling 4.1 NCULDD with AC coupling ..... | 50  | 4.2 電路一-with AC coupling.....           | 50   |
| .....53 4.3 電路二-with AC coupling.....                                       | 56  | 第五章 結論 5.1 設計技術.....                    | 56   |
| .....59 5.2 電路架構.....   | 60  | 5.3 電路規格.....                           | 60   |
| .....61 5.4 功率消耗.....   | 62  | 參考文獻.....                               | 62   |
| .....65   |     |   |      |

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