

n型金氧半場效電晶體速度飽和長度受溫度效應影響之研究=study on the length of velocity saturation by temperature effect inf

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摘要

對金氧半場效電晶體(MOSFET)元件而言，在電性設計時，其「速度飽和長度」及「有效通道長度」皆為必須考量的重要參數之一；且在製程上必須要控制「有效通道長度」是否在設計範圍之內。因此本論文研究的重點在於萃取元件的「速度飽和長度」，並比較當元件在不同溫度與基底偏壓，其速度飽和長度的變化。除此之外，有關臨限電壓隨通道長度異常變化，及熱載子效應等的影響，本文亦有相關的探討。而當元件在飽和偏壓下，若LDD處的電場過大，載子受到飽和速度的影響，會使得R_{total}隨汲極電流或閘極偏壓增加而遞增。本論文以簡單的汲極電流的一次方型式，即其隨ID變化的R_{total}值來萃取速度飽和長度與飽和速度。首先，我們先設計一批不同的通道長度的MOSFET元件，然後利用線性外差法(LE Method)萃取不同溫度與基板偏壓下的臨限電壓，然後將其值代入萃取出 L_{sat}。再則我們使用TMA-MEDICI和TMA-TSUPREM4模擬軟體加以驗證，由實驗得知，速度飽和長度變化是隨通道長度、閘極偏壓增加而遞增；隨溫度上升而遞減。

關鍵詞：速度飽和長度；飽和速度；有效通道長度

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- [1] Sah, C. T., Fundamentals of Solid-State Electronics, World Scientific 1991.
- [2] Wolf, Stanly, Silicon Processing for the VLSI Era volume 3, Lattice Press 1995.
- [3] 張俊文， “金氧半場效電晶體有效通道長度萃取方法之研究”，中正理工學院碩士論文1996。
- [4] Pierret,R.F., Field Effect Device, Addison esley Publishing Company 1989.
- [5] F.F. Fang and A.B. Fowler,J Appl. phys., 1988 , 69,p. 619.
- [6] Y.C.Chen and E.A.Sullivan , “ Effect of Coulombic scattering on silicon surface mobility,” J. Appl. Phys.,1982,45,p.187.

- [7] Y.C.Chen and E.A.Sullivan, IEEE J. Solid-State Circuit., 1979, SC-34, pp. 715-717 [8] B.Hofflinger, H.Sibber, and G.Zimmer,'Model and performance of hot-electron MOS transistor for VLSI, " IEEE Trans. Electron Devices, 1989, ED-26, pp. 513-520.
- [9] A.G.Sabnis and J.T.Clemens, " Characterization of electron velocity in the inverted (100) Si surface, " IEDM Tech. Dig. , 1988, pp.18.
- [10] P. K. KO, " Hot electron effects in MOSFET`s," Ph.D. dissertation, Univ. of Calif., Berkeley, 1982.
- [11] Y.A.El Mansy and A.R.Boothrovd, " A simple-two-dimensional model for IGFET operation in the saturation region " IEEE Trans. Electron Devices, 1989, ED-24, pp. 253-254.
- [12] P.K.Ko et al., " A unified model for hot-electron currents in MOSFET`s, " IEDM Tech. Dig , 1985, p600.
- [13] T.N. Nguyen and J.D.Plummer, " Physical mechanisms responsible for short channel effects in MOS devices, " IEDM Tech. Dig , 1985, p596.
- [14] C. Hu, " Hot electron effects in MOSFET`s, " IEDM Tech. Dig , 1985, p176.
- [15] S. Selberherr, A. Schutz and H.W. Potzl, " MONIMOS A two dimensional MOS transistor analyzer, " IEEE Trans. Electron Devices, 1980, ED-27, pp.1540-1544.
- [16] T. Tsuchiya, Kobayashi and S. Nakajima, IEEE Trans. Electron Devices, 1987, ED-34, pp. 386-390.
- [17] T. Tsuchiya, and J. Frey, IEEE Elec. Dev. Lett., 1985,EDL-6, p.8.
- [18] Y.C. Chen and E.A. Sullivan, Surf. Sci.34, 1983, p. 717.
- [19] Massobrio, G., and Antognetti, P., Semiconductor Device Modeling with SPICE, 2nd Edition, Ch. 4, McGraw-Hill, NY, 1993.
- [20] R.V.H. Booth and M.H. White, " An experimental method for the determination of the saturation point of a MOSFET`s, " IEEE Trans. Electron Devices. , 1984, ED-31, pp. 247-251 [21] W.Y. Jang, C.Y. Wu and H.J. Wu, " A new experimental method to determination the saturation voltage of a small-geometry MOSFET, " Solid-State Electron., 1988, 31, pp. 1421-1431.
- [22] R.J. Schreutelkamp and L. Deferm, " A new method for measuring the saturation velocity of sub-micron CMOS transistor, " Solid-State Electron., 1995, 38, pp. 791-793.
- [23] J. W. Schrankler, J. S. T. Huang, R. S. Lutze, H. P. Vyas and G. D. Kirchner, " Cryogenic behavior of scaled CMOS devices, " in IEDM Tech. Dig., P. 598, Dec. 1984.
- [24] Kiyoshi Takeuchi and Masao Fukuma,Effects of the velocity saturated region on MOSFET characteristics, " IEEE Trans. Electron Devices, 1994, ED-41, P. 1623.
- [25] Robert K Reich, Dong-Hyuk JU and Albert M Sekela, " Velocity saturation limitations of lightly doped drain transistors, " IEEE Trans. Electron Devices, 1988, ED-35, P. 444.
- [26] C.R. Crowell and S.M. Sze, " Temperature dependence of avalanche multiplication in semiconductors, " J Appl. phys. Lett.,1986, vol.9 p. 242.
- [27] VLSI Electron Microstructure Science,vol.18 Chaper 5,p.228.