

HARDWARE DESIGN AND SIMULATION FOR DIGITAL ADAPTIVE BEAMFORMER

陳世軒、劉仁俊

E-mail: 9019016@mail.dyu.edu.tw

ABSTRACT

THE PURPOSE OF ADAPTIVE BEAMFORMING IS TO USE ARRAY SENSORS TO PICK UP SIGNALS ARRIVED FROM VARIOUS DIRECTIONS. THE DIRECTION OF ARRIVALS (DOA) OF DIFFERENT SIGNALS IS THEN DETERMINED. BY CONCENTRATING PROCESSING AT CERTAIN ANGLES OF INTERESTING, THE SYSTEM EFFICIENCY CAN BE GREATLY IMPROVED. THE APPLICATIONS OF BEAMFORMING INCLUDE RADAR, SONAR, SPEECH AND WIRELESS COMMUNICATION. ONE OF THE MOST POPULAR APPROACHES IS OF MVDR WHICH IS BASED ON OPTIMUM FILTERING AND SPECTRUM ANALYSIS. A COST FUNCTION IS FIRST DEFINED AND MINIMIZED IN ORDER TO FIND THE OPTIMAL CONNECTION WEIGHTS BETWEEN THE INPUT SIGNAL FROM THE ANTENNA ARRAY AND BEAMFORMER OUTPUT. THE COMPUTATION OF THE INVERSE CORRELATION MATRIX AND ITS MULTIPLICATION WITH STEERING VECTOR ARE THE MOST IMPORTANT PARTS IN THIS PROCESSES. IN A SYSTEM THAT REQUIRES HIGH-SPEED COMPUTATION, MATRIX INVERSION IS AMONG THE MOST TIME-CONSUMING PROCESSES, ESPECIALLY WHEN THE DIMENSIONALITY IS HIGH AND MATRIX IS CLOSE TO SINGULAR. FORTUNATELY, ADAPTIVE ALGORITHMS ARE AVAILABLE SO THAT THE PROCESS CAN BE PERFORMED EFFICIENTLY IN A RECURSIVE WAY. THIS PAPER WILL DERIVE A FAST ALGORITHM FOR COMPUTING INVERSE CORRELATION MATRIX IN DIGITAL BEAMFORMING USING THE FAMOUS MATRIX INVERSION LEMMA. WE WILL DEMONSTRATE IN THE SIMULATION THAT DOA CAN BE EFFICIENTLY DETECTED USING THIS APPROACH. IN HARDWARE DESIGN, WE USE THE TMS320C6X DIGITAL SIGNAL PROCESSOR FROM TEXAS INSTRUMENT (TI) TO APPLY THE ADAPTIVE BEAMFORMING IN THE DEVELOPMENT OF DSP HARDWARE. THIS IS WILL FACILITATE THE TECHNIQUE OF ADAPTIVE BEAMFORMING IN WIRELESS COMMUNICATION.

Keywords : LINEAR ARRAY、CIRCULAR ARRAY、MVDR、LCMV、CORRELATION MATRIX、RLS、DIGITAL SIGNAL PROCESSOR.

Table of Contents

第一章緒論 1.1 研究背景--P1 1.2 研究動機與目的--P2 1.3 內容大綱--P3 第二章適應性波束形成器 2.1陣列天線信號模式--P4 2.2 還形天線信號模式--P6 2.3 LCMV/MVDR 法則--P8 2.4 遞迴式更新反矩陣--P12 第三章TMS320C6x 數位訊號數理器 3.1 程式發展流程--P15 3.2 TMS320C6x介紹與處理器架構--P17 3.3記憶體分配--P18 3.4 DSP/BIOS--P22 3.5 EMIF 實際應用--P25 第四章模擬結果與硬體實現 4.1 MVDR 法則驗證--P29 4.2不同天線數目之影響--P31 4.3實際量測之天線驗證--P37 4.4硬體實現結果--P43 第五章結論與未來展望 5.1結論--P47 5.2 未來研究方向--P48 參考文獻--P49

REFERENCES

- [1] T.E.CURTIS, "DIGITAL BEAM FORMING FOR SONAR SYSTEM," IEEE PROC. PT.F,VOL.127,PP. 257-265,AUG.1980.
- [2] P.BARTON, "DIGITAL BEAMFORMING FOR RADAR," IEEE PROC. PT.F,VOL.127,PP.266-277,AUG. 1980.
- [3] B. D. VAN VEEN AND K. M. BUCKLEY, "BEAMFORMING: A VERSATILE APPROACH TO SPATIAL FILTERING," IEEE ASSP MAGAZINE, PP. 4-924, APRIL 1988.
- [4] H. KRIM AND M. VIBERG, "TWO DECADES OF ARRAY SIGNAL PROCESSING RESEARCH: THE PARAMETRIC APPROACH," IEEE SIGNAL PROCESSING MAGAZINE, PP. 67-94, JULY 1996.
- [5] J. CAPON, "HIGH-RESOLUTION FREQUENCY-WAVENUMBER SPECTRUM ANALYSIS," PROCEEDINGS OF IEEE, PP. 2408-2418, AUGUST 1969.
- [6] S. HAYKIN, "ADAPTIVE FILTERING," 3RD ED., PRENTICE-HALL, 1996 [7] J.LITVA AND TITUS KWOK-YEUNG LO, "DIGITAL BEAMFORMING IN WIRELESS COMMUNICATION", ARTECH-HOUSE,1996 [8] C. BALANIS, "ANTENNA THEORY, ANALYSIS AND DESIGN," JOHN WILEY & SONS, 1982.

- [9] R. O. SCHMIDT, "A SIGNAL SUBSPACE APPROACH TO MULTIPLE EMITTER LOCATION AND SPECTRAL ESTIMATION," PH.D. DISSERTATION, STANFORD UNIVERSITY, 1981.
- [10] R. ROY AND T. KAILATH, "ESPRIT - ESTIMATION OF SIGNAL PARAMETERS VIA ROTATIONAL INVARIANCE TECHNIQUES," IEEE TRANSACTIONS ON ASSP, VOL. 37, NO. 7, PP. 984-995, JULY, 1989.
- [11] P. CHANG, W. YANG AND K. CHAN, "A NEURAL NETWORK APPROACH TO MVDR BEAMFORMING PROBLEM", IEEE TRANSACTIONS ON ANTENNA AND PROPAGATION, VOL. 40, NO. 3, PP. 313-321, MARCH 1992.
- [12] G. H. GOLUB AND C. F. VAN LOAN, "MATRIX COMPUTATIONS," 2ND ED., JOHN HOPKINS UNIVERSITY PRESS, 1989.
- [13] W. PRESS, B. FLANNERY, S. TEUKOLSKY AND W. VETTERLING, "NUMERICAL RECIPES IN C," CAMBRIDGE UNIVERSITY PRESS, 1988.
- [14] S.P. APPLEBAUM, "ADAPTIVE ARRAY," IEEE TRANS. ON AP, 24, PP. 585-598, 1976 [15] R. WIDROW AND S. STEARNS. ADAPTIVE SIGNAL PROCESSING. ENGLEWOOD CLIFFS, NJ: PRENTICE-HALL, 1985 [16] O.L. FROST III, "AN ALGORITHM FOR LINEARLY CONSTRAINED ADAPTIVE ARRAY PROCESSING," PROC. IEEE, VOL. 60, PP. 926-935, 1972 [17] J.G. MCWHIRTER AND T.J. SHEPHERD, "SYSTOLIC ARRAY PROCESSOR FOR MVDR BEAMFORMING," PROC INST. ELEC. ENG, VOL. 136, NO. 2, 1989.
- [18] W.M GENTLEMAN AND H.T. KUNG, "MATRIX TRIANGULARIZATION BY SYSTOLIC ARRAY," PROC SPIE REAL TIME SIGNAL PROCESSING IV, P. 298, 1981 [19] C.N. DORNY, "A VECTOR SPACE APPROACH TO MODELS AND OPTIMIZATION," WILEY-INTERSCIENCE, NEW YORK, 1975 [20] TMS320C6000 DSP/BIOS USER'S GUIDE (TI LITERATURE NUMBER : SPRU303) [21] TMS320C6000 DSP/BIOS APPLICATION PROGRAMMING INTERFACE (API) REFERENCE GUIDE (TI LITERATURE NUMBER : SPRU403) [22] TMS320C6000 PROGRAMMER'S GUIDE (TI LITERATURE NUMBER : SPRU198) [23] TMS320C6000 TECHNICAL BRIEF (TI LITERATURE NUMBER : SPRU197) [24] TMS320C6000 PERIPHERALS REFERENCE GUIDE (TI LITERATURE NUMBER : SPRU190) [25] TMS320C6000 OPTIMIZING COMPILER USER'S GUIDE (TI LITERATURE NUMBER : SPRU187) [26] TMS320C62C C SOURCE DEBUGGER USER'S GUIDE (TI LITERATURE NUMBER : SPRU188) [27] CODE COMPOSER STUDIO USER'S GUIDE (TI LITERATURE NUMBER : SPRU328) [28] TMS320C6000 CPU AND INSTRUCTION SET REFERENCE GUIDE (TI LITERATURE NUMBER : SPRU189) [29] HOW TO BEGIN DEVELOPMENT TODAY WITH THE TMS320C6211 DSP (TI LITERATURE NUMBER : SPRA474) [30] TMS320C6000 EMIF TO EXTERNAL ASYNCHRONOUS SRAM INTERFACE (TI LITERATURE NUMBER : SPRA542) [31] TMS320C6000 EMIF TO EXTERNAL FLASH MEMORY (TI LITERATURE NUMBER : SPRA568) [32] TMS320C6000 EMIF TO EXTERNAL SDRAM/SGRAM INTERFANCE0 (TI LITERATURE NUMBER : SPRA433A) [33] TMS320C6211 FIXED-POINT DIGITAL SIGNAL PROCESSOR (TI LITERATURE NUMBER : SPRS073A) [34] TMS320C6000 C COMPILER: DEVELOPMENT FLOW (TI LITERATURE NUMBER : SPRA518) [35] DSPBIOS BY DEGREES USING DSPBIOS FEATURES IN AN EXISTING APPLICATION (TI LITERATURE NUMBER : SPRA591) [36] IMPLEMENTATION OF AN ADAPTIVE ANTENNA (TI LITERATURE NUMBER : SPRA532) [37] TMS320C6000 EMIF: OVERVIEW OF SUPPORT OF HIGH PERFORMANCE MEMORY TECHNOLOGY (TI LITERATURE NUMBER : SPRA631) [38] USING DSP/BIOS I/O IN MULTICHANNEL SYSTEMS (TI LITERATURE NUMBER : SPRA689) [39] R. L. PETERSON, R. E. ZIEMER, AND D. E. BORTH "INTRODUCTION TO SPREAD SPECTRUM COMMUNICATIONS," 1995.
- [40] R. STEELE "GSM, CDMAONE & 3G SYSTEM," 2000.
- [41] IMPLEMENTING THE TMS320C6201/C6701/C6211 HPI BOOT PROCESS (TI LITERATURE NUMBER : SPRA512)