

# 導入物件導向與派翠網於嵌入式系統的發展

陳若儀、梁文耀

E-mail: 9015634@mail.dyu.edu.tw

## 摘要

由於嵌入式系統的應用層面相當廣泛，很難找出一種具有代表性的系統模型，因此在設計上就只能依據以往相似的設計經驗或是隨機的選用方法來設計系統。然而這種缺乏正規的設計方式很容易受限於設計者的能力，或因為設計者的經驗不足，使得整個系統無法去探索到各種可能的執行方式，進而求得最佳的效能。為了使系統有完整的分析與設計功能，本文利用物件導向與派翠網的結合來描述嵌入式系統的發展流程，期望能藉由物件，以更接近真實系統的運作方式，來描述嵌入式系統的行為。進而使得整個系統的發展流程可以模組化、標準化，以減少系統開發的時間與風險。除此之外，本文也利用派翠網的可達樹分析，驗證此發展流程的可行性，以確保在發展的流程中不會有死結的狀態，減少錯誤的發生。

關鍵詞：嵌入式系統、物件導向派翠網、可達樹分析、軟硬體共同設計

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## 參考文獻

- 1.ATHANOS, P., AND SILVERMAN, H.F., "PROCESSOR RECONFIGURATION THROUGH INSTRUCTION-SET ME-TAMORPHOSIS," COMPUTER, VOL. 26, NO. 3, PP. 11-18, 1993. 2.CLARK, K. B. AND WHEELWRIGHT, S. C., "MANAGING NEW PRODUCT AND PROCESS DEVELOPMENT," TH -E FREE PRESS, 1993. 3.CLAUDIONOR JOSE NUNES COELHO JR., DIOGENES CECILIO DA SILVA JR., AND ANTONIO OTAVIO FER -NANDES, "HARDWARE-SOFTWARE CODESIGN OF EMBEDDED SYSTEM," INTEGRATED CIRCUIT DESIGN, PP. 2-8, 1998. 4.DANIEL D. GAJSKI, "SPECIFICATION AND DESIGN OF EMBEDDED HARDWARE-SOFTWARE SYSTEMS," IEE -E DESIGN & TEST OF COMPUTERS, PP. 53-67, 1995. 5.DAR-CHIN RAU, CHIEN-YUN DAI AND CHING-WEN CHIOU, "AN OBJECT ORIENTED PETRI NETS TO CONSTRUCT PROTOTYPE OF HYPERMEDIA SYSTEM," PROCEEDING, IASTED INTERNATIONAL CONFERENCE APP-LIED INFORMATICS, AUSTRIA, FEBRUARY 21-23, 1995. 6.DAVID ELEGY, "AN OBJECT MODEL FOR EMBEDDED SYSTEM DEVELOPMENT," NORTHCON, CONFERENCE RECORD, PP. 234-238, 1993. 7.DAVID RENE AND ALLA HASSANE, "PETRI NETS AND GRAFCET," PRENTICE HALL, 1992.
- 8.DENIS MUKHIN, AND BOLESLAW MIKOLAJCZAK, "A METHOD OF CONCURRENT OBJECT-ORIENTED DESIGN USING HIGH-LEVEL PETRI NETS," IEEE INTERNATIONAL CONFERENCE, VOL. 1, PP. 295-300, 1998. 9.ERNST, R., HENKEL, J., AND BENNER, T., "HARDWARE-SOFTWARE COSYNTHESIS FOR MICROCONTROLLERS," IEEE DESIGN & TEST COMPUTER, PP. 64-75, DECEMBER 1993. 10.GAYNOR, G. H., "ACHIEVING THE COMPETITIVE EDGE THROUGH INTEGRATED TECHNOLOGY MANAGEMENT," MCGRAW-HILL, INC., 1991. 11.GEORGE, G. W. AND KRYAL, E. "THE PERCEPTION AND USE OF STANDARDS AND COMPONENTS IN EMBEDDED SOFTWARE DEVELOPMENT - A REPORT FOR THE OMI SOFTWARE ARCHITECTURE FORUM," HTTP://WWW.OSAF.ORG/LIBRARY/MARKET.PDF, JULY 1996.
- 12.HTTP://WWW.IEE.ORG.UK/2000RISK/EMB.HTML 13.J. L. PETERSON, "PETRI NET THEORY AND THE MODELING OF SYSTEMS," PRENTICE-HALL, ENGLEWOOD CLIFFS, N. J., 1981. 14.JORMA ETC. "PRODUCT-BASED SOFTWARE PROCESS IMPROVEMENT FOR EMBEDDED SYSTEMS," EUROMICRO -CONFERENCE, PROCEEDINGS 24TH, VOL. 2, PP.905-912, 1998.
- 15.LU, E., "OBJECT FOR END USERS," BYTE, DECEMBER 1992. 16.MICHAEL BARR, "PROGRAMMING EMBEDDED SYSTEM IN C AND C++," O'REILLY, 1998. 17.PETRI , C. A., "KOMMUNIKATION MIT AUTOMATEN," PH.D. DISSERTATION, UNIVERSITY OF BONN, B-ONN. 1962. 18.S. KUMMAR, "A UNIFIED REPRESENTATION FOR HARDWARE/SOFTWARE CODESIGN," PH.D. DISSERTATION, UNIVERSITY OF VIRGINIA, 1995. 19.MAGUIRE L.P., "ISSUES IN THE DEVELOPMENT OF AN INTEGRATED ENVIRONMENT FOR EMBEDDED SYSTEM DESIGN," MICROPROCESSORS AND MICROSYSTEMS, VOL.23, OCT 11, PP.199-206, 1999. 20.SIBERTIN-BLANE, C. AND R. BASTIDE, "OBJECT-ORIENTED STRUCTURATION FOR HIGH LEVEL PETRI NETS," 11TH CONFERENCE OF APPLICATION AND THEORY OF PETRI NETS. 21.SRIVISTAVA, M.B., AND BRODERSEN, R. W.,

"RAPID-PROTOTYPING OF HARDWARE AND SOFTWARE IN A UNIFIED FRAMEWORK," INTERNATIONAL CONFERENCE ON COMPUTER-AIDED DESIGN, LOS ALAMITOS, CALIFORNIA, PP. 152-155, 1991. 22.T. MURATA, "PETRI NETS: PROPERTIES, ANALYSIS, AND APPLICATIONS," PROCEEDINGS OF THE IEE -E, VOL. 77, NO. 4, PP. 541-580, 1989. 23.W. W. WOLF, "HARDWARE-SOFTWARE CO-DESIGN OF EMBEDDED SYSTEMS", PROCEEDINGS OF THE IEEE, -VOL. 82, PP. 967-989, 1994. 24.WANG, L, AND CHANG Y. J., "THE DEVELOPMENT OF AN OBJECT-ORIENTED PETRI NET MODEL," WORK -ING PAPER W06/93, DEPARTMENT OF INDUSTRIAL ENGINEERING, TUNGHAI UNIVERSITY. 25.Y. K. LEE AND S. J. PARK, "OPNETS: AN OBJECT-ORIENTED HIGH-LEVEL PETRI NET MODEL FOR RE -AL-TIME SYSTEM MODELING," J. SYSTEMS SOFTWARE, VOL. 20, NO. 99, PP. 69-86, 1993 26.王元鴻，嵌入式信號處理的軟硬體共同設計之快速原型，雲林科技大學電子與資訊工程技術研究所碩士論文，民國87年。 27.林木盛，個體式派曲網路做系統發展方法，師範大學工業教育研究所碩士論文，民國83年。 28.張育仁，應用於製造系統控制的階層式物件導向裴氏圖模式的發展，東海大學工業工程研究所碩士論文，民國81年。 29.陳建光，製造資訊系統之整合性個體導向系統分析方法，東海大學工業工程研究所碩士論文，民國82年。 30.陳建富，電力品質監測儀表系統設計，中原大學電機研究所碩士論文，民國88年。 31.黃正志，問題解決式數學科教學軟體解題流程分析 - 運用派翠西網路，師範大學資訊教育研究所碩士論文，民國86年。 32.黃永裕，產品開發管理原則之研究 - 以新竹科學園區電腦及週邊高科技產業為例，交大科技管理研究所碩士論文，民國84年 33.黃炯彰，物件導向裴氏圖現場控制系統發展方法，東海大學工業工程研究所碩士論文，民國84年。