

# Investigation and implementation of efficiency monitoring and reliability assurance for the green energy products

洪志誠、陳雍宗

E-mail: 321854@mail.dyu.edu.tw

## ABSTRACT

The purpose of this thesis is to conduct a series of researches on high efficiency full-bridge Converter and apply it to practical use. In this thesis, IC (Specs: ISL6754) of Zero Voltage Switching (ZVS) was adopted, which has been mainly applied in the field of ZVS full-bridge converter, power supply of telecommunication, wireless station, file server and industrial power system. In addition, ISL6754 can be used for ZVS in pre-stage and in post-stage as synchronization-rectifier for controlling transistors. Therefore, in his study, ISL6754 is applied to Full-Bridge synchronization-rectifier in order to reduce the loss of conduction and enhance the output efficiency rate up to 96% theoretically and practically. This study will further prove the effect of energy saving. To reduce the size of circuit's contents and the efficiency loss of semi-conductor, and to enhance the efficiency of power conversion, L-C resonance circuit power converter is also adopted, which is also recognized as "Soft-Switching Technique," to reduce the power loss of switching with ZVS technology. This paper is one of the case plan. Part of the main power supply for the system to achieve the best power conversion.

Keywords : High-efficiency full bridge power converter、 zero voltage switching、 radio base station power supply、 synchronous rectifier

## Table of Contents

第一章 緒論	1.1 研究背景與目的	1.2 論文架構簡介	
2 第二章 電力轉換器簡介	2.1 DC/DC轉換器簡介	4.2.2 軟、硬切式優缺點	
	7.2.3 轉換器電路控制技術	10.2.3.1 轉換器電路控制技術	12
2.3.2 相移UCC3895簡介	12.2.3.3 PWM方式ISL6754 簡介	20 第三章 同步整流技術	
3.1 同步整流技術	30.3.2 控制同步整流	32 第四章 昇壓式全橋	
轉換器架構之分析	4.1 電路架構分析	35.4.2 各級電路損失分析	
40 第五章 電路元件參數設計	5.1 設計規格參數	43.5.2 切換功率電晶體選擇	
	43.5.3 高頻變壓器設計	44.5.4 輸出電容	45.5.5
UCC3895及ISL6754頻率設計	45.5.6 驅動器IR2110 說明	48.5.7 TL491	
及4N35 光耦合器電路	52 第六章 電路實作及波形量測	6.1 利用UCC3895 設計電路	
56.6.1.1 規劃前級輸入和後級輸出電路	56.6.1.2 規劃UCC3895 開關頻率	59.6.1.3 規劃	
驅動器IR2110 電路	64.6.2 利用ISL6754 設計電路	66.6.2.1 規劃ISL6754 開關	
頻率電路	66.6.2.2 同步整流電路	69 第七章 實驗結果	7.1 前級利用UCC3895
實作輸出結果	74.7.2 前級利用ISL6754 實作輸出結果	76 第八章 結論與未來展望	8.1 實驗
結論	79.8.2 未來展望		81

## REFERENCES

- [1]S. H. Chung, S. Y. Hui and W. H. Wang, " A zero-current-switching PWM fly-back converter with a simple auxiliary switch, " IEEE Transactions on Power Electronics, vol. 14, no. 2, Mar. 1999, pp. 329-342.
- [2]Y. Xi, and P. K. Jain, " A forward converter topology employing a resonant auxiliary circuit to achieve soft switching and power transformer resetting, " IEEE Transactions on Industrial Electronics, vol. 50, no. 1, Feb. 2003, pp. 132-140.
- [3] A. I. Pressman, Switching power supply design, Switchtronix Power, Inc [4] P. Alou , J. A. Cobos, O. Garcia, R. Prieto and J. Uceda, " A new driving scheme for synchronous rectifiers: single winding self-driven synchronous rectification, " IEEE Transactions on Power Electronics, vol. 16, no. 6, Nov. 2001, pp. 803-810.
- [5]B. S. Lim, H. J. Kim, and W. S. Chung, " A self-driven active clamp forward converter using the auxiliary winding of the power transformer, " IEEE Transactions on Circuits and System-II: Express Briefs, vol. 51, no. 10, Oct. 2004, pp. 549-551.
- [6]M. T. Zhang, M. Jovanovic, and Fred C. Y. Lee, " Design considerations and performance evaluations of synchronous rectification in fly-back

- converters, " IEEE Transactions on Power Electronics, vol. 13, no. 3, May. 1998, pp. 538-546.
- [7]N. Yamashita, N. Murakami, and T. Yachi, " Conduction power loss in MOSFET synchronous rectifier with parallel-connected schottky barrier diode, " IEEE Transactions on Power Electronics, vol. 13, no. 4, July 1998, pp. 667-673.
- [8]M. Jovanovic, M. T. Zhang, and Fred C. Lee, " Evaluation of synchronous-rectification efficiency improvement limits in forward converters, " IEEE Transactions on Industrial Electronics, vol. 42. no. 4, Aug. 1995, pp. 387-395.
- [9]Y. Panov and M. Jovanovic, " Design and performance evaluation of low-voltage/high-current DC/DC on-board modules, " IEEE Transactions on Power Electronics, vol. 16, no. 1, Jan. 2001, pp. 26-33.
- [10]B. I. Kwon, S. J. Park, and S. C. Park, " Forward converter analysis by the method of coupling electromagnetic field with hysteresis and circuit equations, " IEEE Transactions on Magnetics, vol. 36, no. 4, July 2000, pp. 1426-1430.
- [11]Y. Xi and P. K. Jain, " A forward converter topology with independently and precisely regulated multiple outputs, " IEEE Transactions on Power Electronics, vol. 18, no. 2, Mar. 2003, pp. 648-658.
- [12]Intersil ISL6754 Data Sheet [13]UCC3895 Data Sheet [14]Li Xiao, Ramesh Oruganti, " Soft switched PWM DC/DC converter with synchronous rectifiers " Telecommunications Energy Conference, 1996. INTELEC '96, 18th International , 6-10 Oct. 1996 pp. 476-484.
- [15]C.S Leu, G. Hua, F.C. Lee and C. Zhou, " Analysis and design of RCD clamp forward converter " High Frequency Power Conversion Conference, May 1992, pp. 198-208.
- [16]Christopher D. Bridge, " Clamp voltage analysis for RCD forward converters " APEC 2000, Fifteenth Annual IEEE, Vol. 2, 6-10 Feb. 2000 pp. 959-965.
- [17]Ching-Shan Leu, " Improved forward topologies for DC-DC applications with built-in input filter " Ph.D. dissertation, Department of Electrical Engineering, Blacksburg, Virginia, Jan. 24, 2006.
- [18]N. Mohan, T. M. Undeland, and W. P. Robbins, Power Electronics: Converters, Applications, and Design, 3rd Ed, John Wiley & Sons, Inc, 2003.
- [19]R.W. Erickson, D. Maksimovic, Fundamentals of Power Electronics, 2nd Ed, Kluwer Academic Publishers, 2001.
- [20]Yingdong Wei, Xiehua Wu, Yilei Gu, Hao Ma, " Wide Range Dual Switch Forward Fly-back Converter with Symmetrical RCD Clamp " Power Electronics Specialists Conference, 2005, IEEE 36th 11-14 Sept. 2005 pp. 420-424.
- [21]M. T. Zhang, M.M. Jovanovic, F. C. Lee, " Analysis and evaluation of interleaving techniques in forward converters " Power Electronics, IEEE Transactions on, vol. 13, Issue 4, July 1998 pp.690-698.