

# A research and application of negative binomial EWMA control chart

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## ABSTRACT

The traditional control chart for nonconformities (called C control chart) assumes that process nonconformities have Poisson distribution. In actuality however, the occurrence of nonconformities does not always observe Poisson distribution. For example, when nonconformities of wafers have clustering phenomenon in semiconductor production process, the process control based on Poisson distribution always underestimates the true average nonconformities and process variance. If the compound Poisson process is taken as the basis for process control, the quality feature could be described accurately. When the process has minor variation, the sensitivity of the exponentially weighted moving average (EWMA) control chart is higher than the C control chart and more accurately reflects the current situation of the process on the control chart. Hence, this study considers Poisson-Gamma compound distribution for the failure mechanism, and takes the Markov chain approach to calculate the average run length required by the EWMA control chart under different design parameters. Moreover, the EWMA control chart of Poisson-Gamma compound distribution was constructed and actual data from a wafer plant were employed to illustrate the model's working. This study could be used for detecting minor process variations in wafer plants and improving the process quality.

Keywords : nonconformities; Compound Poisson distribution; Average run length; EWMA control chart

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## REFERENCES

中文部份: 1. Hogg, McKean, Craing原著，呂建霖譯，數理統計學，臺灣培生教育出版，台北市，2008。 2. Montgomery原著，徐世輝譯，品質管理，高力圖書有限公司，台北縣，2006。 3. 陳慶文、吳一聲和蘇國璋，指數加權移動平均管制法在幾何卜瓦松製程中之品管設計與使用，品質學報，13(1)，85 - 97(2006)。 4. 劉淑範、陳飛龍，晶圓良率損失資料分群模式之研究，工業工程學刊，21(4)，328 - 338(2004)。 5. 許志偉，整合缺陷點數與群聚指標之積體電路良率模式，交通大學工業工程研究所碩士論文系，2003。 6. 鄭春生、鄭盛樹，管制具群聚現象不合格點數之累和管制法，工業工程學刊，18(6)，1 - 8(2001)。 7. 黃志力，電路生產線上結合缺陷數與群聚指標Hotelling 多變量管制圖，交通大學工業工程研究所碩士論文，2001。 8. 吳炤華，電路生產線上結合缺陷數與缺陷群聚Hotelling 多變量管制圖，交通大學工業工程研究所碩士論文，2000。 9. 梁瑞明，資料群集處理技術在半導體良率預測上之應用，元智大學工業工程研究所碩士論文，2000。 10. 張哲彰，以類神經網路為基礎發展製程管制程序監控具有群聚現象之產品缺點，元智大學工業工程研究所碩士論文，1999。 11. 趙豐昌，利用類神經網路構建之積體電路良率預估模式，交通大學工業工程與管理學系碩士論文，1997。 12. 王永慶，電路生產線上利用類神經網路方法修正缺陷群聚現象之管制圖，交通大學工業工程研究所碩士論文，1996。 13. 楊月美，生產線

上考慮缺陷群聚現象管制程序之研究，交通大學工業工程與管理學系碩士論文，1996。 14.范秋玉，群聚不良品的管制圖探討，清華大學統計學研究所碩士論文，1995。 15.劉宗明，電路生產線上利用群聚分析之修正缺點數管制圖，交通大學工業工程研究所碩士論文，1995。 16.鄭春生，品質管理，育有圖書有限公司，台北，1995。 17.曾乙弘，積體電路生產線上考慮缺陷群聚現象的製程管制圖，交通大學工業工程研究所碩士論文，1994。 英文部分: 18.Albin, S. L. and D. J. Friedman, " Impact of Clustered Defect Distributions in IC Fabrication," *Management Science*, 35(9), 1066 - 0781 (1989). 19.Albin, S. L. and D. J. Friedman, " Clustered Defect in IC:Impact on Process Control Chatsr," *IEEE Transactions on Semiconductor Manufacturing*, 14(1), 36 - 42 (1991). 20. Barnard, G. A., " Control Charts and Stochastic Processes," *Journal of Royal Statistical Society*, 21, 239 - 271(1959). 21. Borrer, C. M., C. W. Champ, and S. E. Rigdon, " Poisson EWMA Control Charts," *Journal of Quality Technology*, 30(4), 352 - 361(1998). 22.Brook, D. and D. A. Evans, " An Approach to the Probability Distribution of CUSUM Run Length," *Biometrika*, 59(3), 539 - 549(1972). 23. Brush, G., G. Hoadley, and B. Saperstein, " Estimating Outgoing Quality Using the Quality Measurement Plan," *Technometrics*, 32, 31 - 41(1990). 24.Champ C. W. and S. E. Rigdon, " A Comparison of the Markov Chain and the Integral Equation Approaches for Evaluating the Run Length Distribution of Quality Control Charts," *Communication in Statistics: Simulation and Computation*, 20(1), 191 - 204(1991). 25. Chao, L. C. and L. I. Tong, " Novel Yield Model for Integrated Circuits with Clustered Defects," *Expert Systems with Applications*, 34, 2334 - 2341(2008). 26.Chen, C. W., Paul. H. Randolph, and T. S. Liou, " Using CUSUM Control Schemes for Monitoring Quality Levels in Compound Poisson Production Environment:The Geometric Poisson Process," *Quality Engineering*, 17, 207 – 217(2005). 27.Cunningham, J. A., " The Use and Evaluation of Yield Models Integrated Circuit Manufacturing," *IEEE Transactions on Semiconductor Manufacturing*, 3(2), 60 - 71(1990). 28. Friedman, D. J., M. H. Hansen, V. N. Nair, and D. A. James, " Model-free Estimation of Defect Clustering in Integrated Circuit Fabrication," *IEEE Transactions on Semiconductor Manufacturing*, 10(3), 344 - 359(1997). 29.Grant, E. W. and R. S. Leavenworth, *Statistical Quality Control*, McGraw Hill Book Co, New York, (1988). 30.Hsieh, K. L., L. I. Tong, and M. C. Wang, " The Application of Control Chart for Defects and Defect Clustering in IC Manufacturing Based on Fuzzy Theory," *Expert Systems with Applications*, 32, 765 - 776 (2007). 31.Lucas, J. M. amd M. S. Saccucci, " Exponentially Weighted Moving Averag Control Schemes:Properties and Enhacements," *Technometrics*, 32(1), 1 - 29 (1990). 32.Murphy, B. T., " Cost-size Optimum of Monolithic Integrated Circuits," *Proceeding of the IEEE*, 52, 1537 - 1545(1964). 33.Peter, V.Z., *Microchip Fabrication*, McGraw-Hill, New York, (1996). 34.Petros, E. M. and P. Castagliola, " An EWMA Chart for Monitoring the Process Standard Deviation when Parameters are Estimated," *Computational Statistics and Data Analysis*, 53, 2653 - 2664(2009). 35. Rainer Winkelmann, *Econometric Analysis of Count Data*, Germany, 2008 36. Seeds, R. B., " Yield and Cost Analysis of Bipolar LSI," Presented at IEEE International Electron Meeting, Washington, D. C, (1967). 37.Stapper, C. H., " Defect Density Distribution for LSI Yield Calculations," *IEEE Transactions on Electron Devices (Correspondence)*, 20, 655 - 657 (1973). 38. Stapper, C. H., " The Effects of Wafer to Wafer Defect Density Variations on Integrated Circuit Defect and Fault Distributions," *IBM Journal of Research Development*, 29(1), 87 - 97(1985). 39.Stapper, C. H., " Large Area Fault Clusters and Fault Tolerance in VLSI Circuits:A Review," *IBMJ. RES. DEVELOP*, 33(2), 162 - 173(1989). 40.Stapper, C. H., " Small-Area Fault Clusters and Fault Tolerance in VLSI Circuits," *IBMJ. RES. DEVELOP.*, 33(2), 174 - 177(1989). 41. Stapper, C. H., " LSI Yield Modeling and Process Monitoring," *IBMJ. Res. Develop*, 40(1/2), 112 - 118(2000). 42. Vargas, V. C., L. F. D. Lopes, and A. M. Souza, " Comparative Study of the Performance of the CuSum and EWMA Control Charts," *Computers and Industrial Engineering*, 46, 707 - 724(2004).